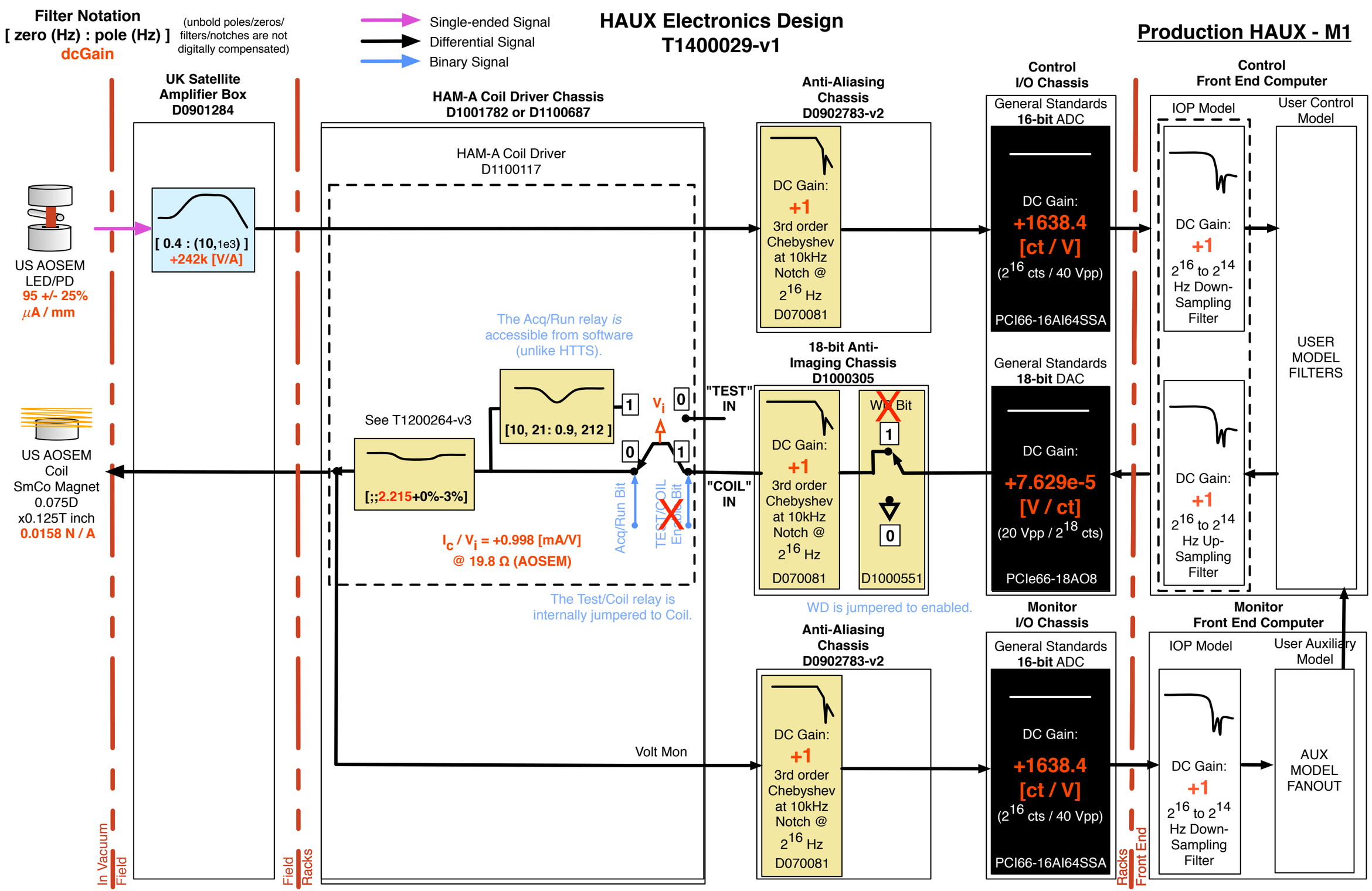
Filter Notation
[zero (Hz) : pole (Hz)]
dcGain
(unbold poles/zeros/ filters/notches are not digitally compensated)
Single-ended Signal
Differential Signal
Binary Signal
HAUX Electronics Design
T1400029-v1
Production HAUX - M1
UK Satellite Amplifier Box D0901284
HAM-A Coil Driver Chassis D1001782 or D1100687
HAM-A Coil Driver D1100117
Anti-Aliasing Chassis D0902783-v2
Control I/O Chassis
Control Front End Computer
IOP Model
User Control Model
General Standards 16-bit ADC
DC Gain: +1638.4 [ct / V] (2^16 cts / 40 Vpp)
PCI66-16AI64SSA
DC Gain: +1
3rd order Chebyshev at 10kHz Notch @ 2^16 Hz
D070081
DC Gain: +1
2^16 to 2^14 Hz Down-Sampling Filter
USER MODEL FILTERS
US AOSEM LED/PD
95 +/- 25%
μA / mm
[0.4 : (10,1e3)]
+242k [V/A]
The Acq/Run relay is accessible from software (unlike HTTS).
18-bit Anti-Imaging Chassis D1000305
General Standards 18-bit DAC
[10, 21: 0.9, 212]
"TEST" IN
WD Bit
DC Gain: +7.629e-5 [V / ct] (20 Vpp / 2^18 cts)
PCIe66-18AO8
DC Gain: +1
2^16 to 2^14 Hz Up-Sampling Filter
US AOSEM Coil
SmCo Magnet
0.075D
x0.125T inch
0.0158 N / A
See T1200264-v3
[;;2.215+0%-3%]
"COIL" IN
D1000551
Acq/Run Bit
TEST/COIL Enable Bit
I_c / V_i = +0.998 [mA/V]
@ 19.8 Ω (AOSEM)
The Test/Coil relay is internally jumpered to Coil.
WD is jumpered to enabled.
Monitor I/O Chassis
Monitor Front End Computer
Anti-Aliasing Chassis D0902783-v2
General Standards 16-bit ADC
IOP Model
User Auxiliary Model
Volt Mon
DC Gain: +1
3rd order Chebyshev at 10kHz Notch @ 2^16 Hz
D070081
DC Gain: +1638.4 [ct / V] (2^16 cts / 40 Vpp)
PCI66-16AI64SSA
DC Gain: +1
2^16 to 2^14 Hz Down-Sampling Filter
AUX MODEL FANOUT
In Vacuum
Field
Field
Racks
Racks
Front End