

**LASER INTERFEROMETER GRAVITATIONAL WAVE
OBSERVATORY**

-LIGO-

**CALIFORNIA INSTITUTE OF TECHNOLOGY
MASSACHUSETTS INSTITUTE OF TECHNOLOGY**

Document Type	DCC Number	July 20, 2011
Common Mode Servo Test Procedure		
Original Author: Modified by:		

Distribution of this draft:
This is an internal working note of the LIGO Laboratory

California Institute of Technology

LIGO Project – MS 18-33

Pasadena, CA 91125

Phone (626) 395-2129

Fax (626) 304-9834

E-mail: info@ligo.caltech.edu

Massachusetts Institute of Technology

LIGO Project – MS 20B-145

Cambridge, MA 01239

Phone (617) 253-4824

Fax (617) 253-7014

E-mail: info@ligo.mit.edu

www: <http://www.ligo.caltech.edu/>

Table of Contents

Test Preparation	3
Required Test and Ancillary Equipment	3
Important Notes	4
Tests Part 1	5
Power Board Voltage	5
Power Supplies Test.....	5
Oscillations.....	6
Adjust DC Bias.....	7
Signal Gain.....	8
Crossbar Switches.....	10
Excitation A.....	10
Split.....	11
Latching.....	11
Excitation B.....	12
Limiter.....	12
Gain Slider C.....	13
EPICS Readbacks.....	13
Limit Indicator.....	14
Tests Part 2	14
Important Notes	14
Power Board Noise.....	15
Monitor Channel Filtering.....	15
Adjustment Channel Filtering.....	16
Distortion.....	16
Noise Spectra.....	16
Basic Transfer Functions.....	17
Transfer Functions of Boost Gain Stages.....	18
Transfer Functions of DAQ Channels.....	18
Tests Part 3	19
High Frequency Transfer Function.....	19

Author:	Dani Atkinson / Daniel Sigg
Refer to:	LIGO-E1100429
Date:	April 29, 2011

Common Mode Servo Board Test Procedure

Test Preparation

Enter Name, Date, Revision, Board Serial Number and board to be tested: common mode board (CM), mode cleaner board (**MC**) or an acquisition light system board (**ALS**).

Test Engineer	Date	Pass
R. Dodd	16 Sep 11	
Board	Board Serial Number	51102643
D040180 rev. E	CM or ALS or MC	

Required Test and Ancillary Equipment

- 1 - Common Mode Board D1003364 Tester
- 1 - Tektronix AFG 3101 Signal Generator or equivalent
- 1 - Tektronix TDS 210 Oscilloscope or equivalent
- 1 - Fluke Multimeter or equivalent
- 1 - HP 4395A Network analyzer (1Hz to 10MHz) or equivalent
- 1 - Stanford Research Systems Signal Analyzer Model SR785
- 1 - GPIB to Cat5 adapter
- 1 - Cat5 cable
- 1 - Laptop using Windows operating system
- 1 - Folder containing Test File Scripts
- 2 - DC Power Supplies (Five Channels Required. Continuous Supply Voltages: +/- 24VDC, +/- 17VDC, and +5VDC)
- 1 - 17VDC Power Cable
- 1 - 24VDC Power Cable
- 1 - 5VDC Power Cable (Banana Plug to Banana Plug Cable and Jumper)
- 1 - custom cable adapting the DB9 Monitor port on the D0901781 front panel into three BNCs. (Refer to Common Mode Board: DAQ, Number D040180 Rev E, Sheet 17 of 17 for DB9 pinout detail)
- 3 - BNC Female to Female Adapters (Barrels)
- 1 - BNC Tee Connector
- 3 - BNC Female to Double Stacking Banana Plugs
- 1 - BNC Male to Mini Grabber Test Leads Cable
- 2 - 50 ohm BNC terminations
- 4 - BNC Male to BNC Male Cables at minimal length

IMPORTANT NOTES:

1. On the Common Mode Servo Tester (D1003364) front panel, all switches must be returned to default positions after each test and/or step, unless otherwise instructed.
2. The default position for most switches is UP, with the exception of switches D22, D25, D28, and D31, which are DOWN.

The switch default positions are shown in Picture 1 below.



Picture 1

Front of D0901781 Common Mode Servo and D1003364 Common Mode Servo Tester in default configuration.

NOTE: Common Mode Servo ALS and MC Variants

1. Unless otherwise marked, nominal values listed are for all boards. Where the ALS and/or MC boards vary from the CM board (black), those values will be green for ALS or red for MC.

Tests Part 1.

Power Board Voltage (Low Noise Power Circuit Board Assembly D0901846)

Connect +/-17VDC and +/- 24VDC to the Common Mode Servo and +5VDC to the Common Mode Servo Tester.

Turn ON Power Supplies.

On the Low Noise Power Circuit Board Assembly, **Connect** the positive multimeter test lead to the following test points and **Connect** the negative multimeter test lead to GRD.

Record the observed voltages in the data boxes below.

Turn Off Power Supplies.

TP1	TP2	TP3	TP4	TP5	TP6	TP7	TP8	TP9	TP10	TP11	TP12	TP13
+17.07	-17.14	0.0	0.0	+5.0	-14.99	+24.05	0.0	-23.99	0.0	+14.99	+10.00	-9.99
+17V	-17V	GND	GND	+5V	-15V	+24V	GND	-24V	GND	+15V	+VREF	-VREF

** Correct voltage indications are: TP14 ~3VDC and front panel OK light lit.

Power Supplies

Turn OFF Power Supplies.

Connect 50 pin Control cables 1 and 2 to corresponding Control Mode Servo Tester and Common Mode Servo jacks.

Turn ON Power Supplies

Check current draw from the ±17V power supply is between 0.3A and 0.6A.

On the front panel of Power Supplies, **Observe** and **Record** the amperage displayed.

Power supply	Current	Nominal
+24V	0.02	0.02
-24V	0.02	0.02
+17V	0.44	.45
-17V	0.55	.45

Oscillations

Connect oscilloscope and **Set** oscilloscope coupling to **AC Coupling**.

Connect oscilloscope probe to the following outputs. Ensure no oscillating wave forms are observed.

Place checkmark in corresponding box below each output.

Outputs	OUT1	OUT2	SERVO	A:TST1	A:TST2	B:TST1	B:TST2
CheckBox	✓	✓	✓	✓	✓	✓	✓
Outputs	D32 Input Mon	D33 Split Mon	D34 Fast Mon	D39 Slow FB Mon	D40 Output Mon		
CheckBox	✓	✓	✓	✓	✓		
Outputs	IMON	FMON	SMON				
CheckBox	✓	✓	✓				

Adjust DC Bias

Set Oscilloscope coupling to **DC Coupling**.

Connect Input Mon (D32) and Offset Adj. (D36) to the oscilloscope.

Ground IN1 using a BNC 50 ohm termination.

Adjust DC bias (R54) for zero volts observed at Input Mon (D32) ensure D32 remains zero when D36 is removed.

Connect FB Mon (D39) and Offset Adj. (D37) to oscilloscope.

Adjust R137 to zero volts observed at FB Mon (D39) when D37 is removed.

Connect OUT1 to oscilloscope.

Turn ON D15 (switch down).

Adjust R54 for zero volts observed.

Return D15 to default position.

Turn ON D16 (switch down).

Adjust R54 for zero volts observed.

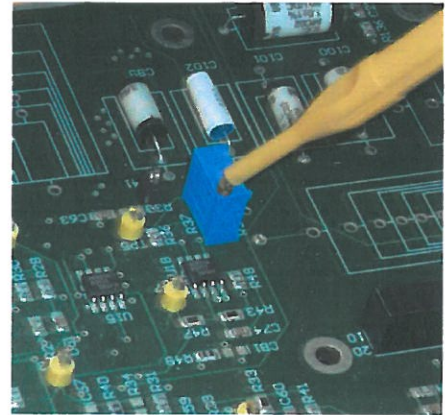
Turn ON D15 and D16.

Adjust R54 for zero volts observed at OUT1.

Return switches to default positions.

Record observations below.

Zero D32 via R54.	0.00 VDC
Zero D39 via R137.	0.00 VDC
Zero OUT1 via R54 with D15 enabled.	0.00 VDC
Zero OUT1 via R54 with D16 enabled.	0.00 VDC
Zero OUT1 via R54 with both D15 and D16 enabled.	0.00 VDC



Picture 2

Signal Gain

Gain slider A (Gain IN1):

Toggle switch D14 **Down** (IN1 position).

Connect OUT1 to the oscilloscope.

Connect Function Generator Output to Common Mode Servo IN1 jack.

Set Function Generator to frequency 100Hz, **Sine wave** and an Amplitude of 1 Vpp.

Inject a 100Hz / 1Vpp **Sine wave** signal. Adjust oscilloscope SEC/DIV until sine wave appears.

Measure the voltage at 0dB (all switches in default position) and **Record**.

Individually, **Toggle** each switch down (GND) and **Record** observed voltage. After each voltage observation, **Return** the switch to default position.

Continue to **Toggle** each switch, **Record** the observed voltage and **Return** each switch to default position.

Return D14 to the default position.

** Tolerance is + / - 0.5dB.

Binary input (Switch Setting)	Measured Vp	Nominal Vp
—(0dB)	0.96	1
D0 (1dB)	1.08	1.12
D1 (2dB)	1.19	1.26
D2 (4dB)	1.50	1.59
D3 (8dB)	2.34	2.51
D4 (16dB)	6.0	6.31
D3 & D4 (24dB)	11.4	15.9
D5 (-32dB)	0.0246	0.025
D5 & D3 (-24dB)	0.060	0.063
D5 & D4 (-16dB)	0.151	0.159
D5 & D3 & D4 (-8dB)	0.372	0.398

Gain slider B (Gain IN2):

Toggle switch D13 down (D14 is in the default position IN2).

Connect OUT2 to an oscilloscope.

Connect Function Generator Output to Common Mode Servo IN2 jack.

Set Function Generator to frequency 100Hz, **Sine wave** and an Amplitude of 1 Vpp.

Inject a 100Hz / 1Vpp **Sine wave** signal into IN2.

Measure the voltage at 0dB (all switches in default position) and **Record**.

Toggle each switch individually **Down** (GND) and **Record** observed voltage. **Return** the switch to default position.

Continue to **Toggle** each switch, **Record** the observed voltage and **Return** each switch to default position.

Return D13 to the default position.

** Tolerance is + / - 1.059 V (+/-0.5dB).

Binary Input (slider gain)	Measured Vp	Nominal Vp
—	1.01	1
D6 (1dB)	1.12	1.12
D7 (2dB)	1.25	1.26
D8 (4dB)	1.59	1.59
D9 (8dB)	2.48	2.51
D10 (16dB)	6.30	6.31
D9 & D10 (24dB)	14.1	15.9
D11 (-32dB)	0.0258	0.025
D11 & D9 (-24dB)	0.0635	0.063
D11 & D10 (-16dB)	0.159	0.159
D11 & D9 & D10 (-8dB)	0.394	0.398

Crossbar switches

Inject a 100Hz/1Vpp Sine wave to IN1. Individually, **Toggle** each Crossbar switches **Down**. Using an oscilloscope, **Record** the voltage states at OUT1 and OUT2. Voltage states are either **ON** or **OFF**.

Binary input	OUT1	Nominal	OUT2	Nominal
Switches in Default Positions	<i>ON</i>	On	<i>off</i>	Off
D12 (input 1 disabled)	<i>off</i>	Off	<i>off</i>	Off
D13 (input 2 enabled)	<i>ON</i>	On	<i>off</i>	Off
D14 (output switch)	<i>ON</i>	On	<i>ON</i>	On

Inject a 100Hz/1Vpp Sine wave to IN2. **Record** the voltage states at OUT1 and OUT2 while toggling the switches **Down**. Voltages states are either **ON** or **OFF**.

Binary input	OUT1	Nominal	OUT2	Nominal
Switches in Default Positions	<i>off</i>	Off	<i>ON</i>	On
D12 (input 1 disabled)	<i>off</i>	Off	<i>ON</i>	On
D13 (input 2 enabled)	<i>ON</i>	On	<i>ON</i>	On
D14 (output switch)	<i>off</i>	Off	<i>off</i>	Off

Excitation A

Inject a 100Hz/1Vpp Sine wave to IN1. **Measure** and **Record** the voltage at A:TEST1 and A:TEST2. ** Tolerance is +/-0.5dB.

Binary input	A:TEST1	Nominal Vp	A:TEST2	Nominal Vp
Switches in Default	<i>-1.00</i>	-1.00	<i>1.00</i>	1.00

Inject a 100Hz/1Vpp Sine wave to A:EXC. **Measure** and **Record** the voltage at A:TEST2 and OUT1. ** Tolerance is +/-0.5dB. (Red = MC)

Binary input	A:TEST2	Nominal Vp	OUT1	Nominal Vp
Default	<i>off</i>	Off	<i>off</i>	Off
D18 (com exc enable)	<i>-0.10</i>	-0.10	<i>1.0</i>	0.10 / 0.50
D18 & D19 (com option)	<i>-0.10</i>	-0.10	<i>off</i>	Off

Split

Set O'scope to DC coupling. Inject a 100Hz/1Vpp Sine wave to IN1. Measure and Record the voltage at OUT1 and SERVO while toggling the switches **Down**. ** Tolerance is +/-0.5dB.

Binary input	OUT1	Nominal Vp	SERVO	Nominal Vp
—	-1.00	-1.00/ -5.0	-1.00	-1.00
Lift D22 (disable fast)	-1.00	-1.00/ -5.0	off	Off
D21 (common filter)	-1.00	-1.00/ -14.5	-1.00	-1.00
D23 (fast polarity)	-1.00	-1.00/ -5.0	1.00	+1.00
D20 (slow polarity)	-1.00	-1.00/ -5.0	1.00	+1.00
D24 (slow option)	off	Off	-1.00	-1.00
Lift D28 (slow comp)	3.00	3.98/ -14.5 (phase offset)	-1.00	-1.00
D29 (slow boost)	3.80	4.12/ -14.5 (phase offset)	-1.00	-1.00
D30 (slow filter)	-1.00	-1.00/ -5.0	-1.00	-1.00
D25 (slow bypass)	-1.00	-1.00/ -5.0	1.00	+1.00
D27 (slow offset enable)	-1.00	-1.00/ -5.0 (change offset with slow offset D38)	-1.00	-1.00
D27 and D26 (slow 5V offset)	-1.00	-1.00/ -5.0 (5 V offset)	-1.00	-1.00

Latching

Inject a 100Hz/1Vpp Sine wave to IN1. Toggle Down LE switch (P1/11 latch enable). Measure and Record the voltage at SERVO.

Toggle D12 Down (IN1 1 enable) and make sure the signal at the output stays on all the time.

Return LE switch and D12 switch to default positions.

SERVO	2.00 Vpp
Check	✓

Excitation B

Inject a 100Hz/1Vp **Sine wave** to IN1. **Measure** the voltage at B:TEST1 and B:TEST2 while toggling the switches **Down**. Tolerance is +/-0.5dB.

Binary input	B:TEST1	Nominal Vp	B:TEST2	Nominal Vp
—	-1.0	-1.00	+1.0	1.00
Lift D22	off	Off	off	Off
D49 (fast/slow) and lift D22	-1.0	-1.00	1.0	1.00

Inject a 100Hz/1Vp **Sine wave** to B:EXC. **Measure** the voltage at OUT1 and SERVO while toggling the switches **Down**. Tolerance is +/-0.5dB.

Binary input	OUT1	Nominal Vp	SERVO	Nominal Vp
—	off	Off	off	Off
D47 (exc. enable)	off	Off	0.10	0.10
D47 & D48 (fast option)	off	Off	off	Off
D47 & D49	0.10	0.10	off	Off
D47, D49, & D24	off	Off	off	Off

Limiter

Inject a 100Hz/10Vpp **Sine wave** to IN1. **Measure** the voltage at SERVO while toggling switch D31/35 **UP** (on the tester, red is on and green is off for this switch). The measured voltage should be within 25% of the nominal value.

Binary input	Measured Vpp	Nominal Vpp
-	19.2	20.0 Vpp
D31 (fast limiter)	7.6	6.6 Vpp

Gain slider C

Inject a 100Hz/1Vp **Sine wave** to IN1. **Measure** the voltage at SERVO while toggling the switches **Down**. Tolerance is +/-0.5dB.

Binary input (slider gain)	Measured Vp	Nominal Vp
—	0.99	1
D41 (1dB)	1.11	1.12
D42 (2dB)	1.22	1.26
D43 (4dB)	1.56	1.59
D44 (8dB)	2.44	2.51
D45 (16dB)	6.20	6.31
D44 & D45 (24dB)	14.0	15.9
D46 (-32dB)	0.0254	0.025
D46 & D44 (-24dB)	0.0625	0.063
D46 & D45 (-16dB)	0.156	0.159
D46 & D45 & D44 (-8dB)	0.386	0.398

EPICS Readbacks

Inject a 1Hz/1Vpp **Sine wave** to IN1. **Observe** analog outputs for a peak to peak value and **Record** the observed voltage.

Inject a 100Hz/1Vpp **Sine wave** to IN1 and **Record** the observed voltage.

**The voltage tolerance is 1 dB (6dB for D34) of the nominal value.

(Red = MC) (Green = ALS)

EPICS readback	1Hz	Nominal Vpp	100Hz	Nominal Vpp
D32 (input mon)	-0.99	-1.00	0.080	0.080
D33 (split mon)	-0.99	-1.00	0.080	0.080
D34 (fast mon)	2.4	-0.4 / 7.5 / 7.5	0.80	0.80
D39 (slow FB mon)	0.96	1.00		
D40 (output mon)	-0.96	-1.00		

Limit indicator

Inject a 1Hz/10Vpp **Square wave** to IN1. **Observe** D35 Indicator Light (limit indicator) is **ON** and **Record** the observed voltage. Compare with the nominal response; see Appendix A6.

D35 Indicator Light Check	✓
Voltage	19.10 Vpp

Inject a 100Hz **Sine wave** to IN1. Increase injected signal amplitude from 0.0V, in 0.1V steps, until D35 Indicator Light goes from high (**ON**) to low (**OFF**). **Record** the observed voltage.

Binary input	Measured [Vpp]	Nominal [Vpp]
—	6.2 Vpp	Approx. 6.0 Vpp

Tests Part 2: SR785 Signal Analyzer Tests

Important Notes: 1. Ensure all Common Mode Servo Tester switches are in the default position. 2. Closely Read and follow all On-Screen prompts.

On a Windows operating system laptop, **Create** and **Save** a file called TEST_DATA to C: drive. The path is C:\Test_DATA\.

Save Test Scripts in TEST_DATA.

Connect an SR785 Signal Analyzer to the laptop with a GPIB to Cat5 adapter.

From the DOS CMD window, **Type** cd.. , **Enter**, **Type** cd.. , **Enter** and **Type** cd TEST_DATA.

Type and **Run** 'setgpib.bat' and **Enter** the adapter's IP address (which should be labeled on the adapter).

Reset the SR785's settings with 'resetSR785.bat'. If the SR785 resets when the script is run, the SR785 is properly connected to the PC.

Power Board Noise (SR785PowerBoardNoise.bat)

One pair of probes (MiniGrabbers) are required to **check the noise levels at 140Hz** on the Low Noise Power Board.

In the DOS CMD window, **Type** SR785PowerBoardNoise.

Read and Follow the On-Screen prompts for proper test equipment configuration and procedure.

Record the collected On-Screen data in the boxes below and remove the probes.

** Test values must be less than the values indicated in the table below.

TP12	< [nV/√Hz]	TP13	< [nV/√Hz]	TP11	< [nV/√Hz]	TP6	< [nV/√Hz]
2.25	20	22.55	30	14.20	30	24.78	30

Monitor Channel Filtering (SR785MonitorTFs.bat)

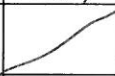
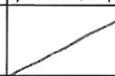
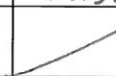
Ensure all switches are in default positions.

In the DOS CMD window, **Type** SR785MonitorTFs

Read and Follow the On-Screen prompts for proper test equipment configuration and procedure.

Measure test transfer functions from 1Hz to 100Hz on IN1 to the indicated monitor channels on the tester and **Record** the data in the table below.

** Tolerances for Lowpass filtering are +/-1dB and +/-5deg from nominal.

Boost #	@1Hz	Nominal	@10Hz	Nominal	@100Hz	Nominal
Input Mon (D32)	-0.25 163.75	-0.1dB 173deg	-4.43 127.48	-4.1dB 129deg	-22.34 94.38	-22dB 95deg
Split Mon (D33)	-0.34 172.9	-0.1dB 173deg	-4.42 128.34	-4.1dB 129deg	-22.33 94.50	-22dB 95deg
Fast Mon (D34) (CM)		-8.8dB 150deg		10.5dB 5deg		-2.5dB -79deg
D34 (MC/ALS)	19.37 -7.20	19.9dB 173/-7/-7deg	15.28 -51.74	15.9dB 128/-51/-51deg	-2.48 -80.57	-2.0dB 98/-85/-85deg
FB Mon (D39)	-0.56 -7.29	-0.1dB -7deg	-4.67 -51.86	-4.1dB -51deg	-22.61 -85.35	-22dB -85deg
Output Mon (D40)	-0.56 172.74	-0.1dB 173deg	-4.66 126.94	-4.1dB 129deg	22.81 82.39	-22dB 95deg

Adjustment Channel Filtering (SR785AdjustmentTFs.bat)

Type SR785AdjustmentTFs

Test the transfer functions from 1Hz to 10KHz on the indicated adjustment channels on the tester to OUT1. **Toggle Down D27 when testing D38**. Verify filtering of at least -60dB at 100Hz for each channel and **Record** levels below in the boxes below.

Return switch D27 to default position.

Offset Adj.(D36)	-99.62	Offset Adj. (D37)	-85.53	Output Adj. (D38)	-78.32
------------------	--------	-------------------	--------	-------------------	--------

Distortion (SR785DistortionMeasurement.bat)

Reset SR785. Type resetSR785. Type SR785DistortionMeasurement

Inject a 1kHz/1Vrms sine wave to IN1. Follow the on-screen prompts. The THD values are displayed after closing the graphics display window. **Repeat** the measurement for IN2 (**D13 Down (ON) and D12 is Down (OFF)**). **Record** the measurements below.

Return D12 and D13 to the default positions.

	IN1	SERVO	IN2	SERVO
Total Harmonic Distortion (THD)	-82.25	<-70dB	-78.69	<-70dB

Noise Spectra (SR785NoiseMeasurements.bat)

Ensure all switches are in Default positions. **Reset the SR785. Type resetSR785.**

Type SR785NoiseMeasurements and **Press Enter**. Follow the displayed script.

Terminate IN1 and IN2 using 50 ohm terminations. **Record** the values at 100Hz, 1kHz and 10kHz in the table below.

Frequency	OUT1	< [nV/√Hz]	OUT2	< [nV/√Hz]	SERVO	< [nV/√Hz]
100Hz	102.4	40	19.2	30	124.8	50
1kHz	19.6	30	17.6	30	53	40
10kHz	6.3	30	18.7	30	33.6	40

Basic Transfer Functions (SR785BasicTFs.bat)

Ensure all switches are in default positions. Type SR785BasicTFs and Press Enter

Sweep the frequency from 100kHz down to 1Hz with 100mV source amplitude and Measure the transfer function from IN1 to OUT1, from IN1 to SERVO and from IN2 to OUT2. Record the values at 10Hz, 100Hz, 1kHz, 10kHz and 100kHz in the table below. See Appendix A2 for typical examples.

** Tolerances must be within 1dB and 5deg of nominal. See Appendix A2 for typical examples.

OUT1/IN1	dB	Nom (CM MC ALS)	deg	Nom (CM MC ALS)
1Hz	-0.49	0.0dB 14.0dB 0.0dB	180.0	180deg 180deg 180deg
10Hz	-0.51	0.0dB 14.0dB 0.0dB	178.6	180deg 180deg 179deg
100Hz	-0.75	0.0dB 14.0dB -0.2dB	167.8	180deg 177deg 169deg
1kHz	-8.07	0.0dB 13.0dB -7.0dB	114.6	180deg 153deg 117deg
10kHz	-27.25	0.0dB -0.2dB -26.0dB	91.65	175deg 102deg 94deg
100kHz	-46.9	-3.0dB -20.0dB -46.0dB	81.67	130deg 86deg 85deg

SERVO/IN1	dB	Nom (CM MC ALS)	deg	Nom (CM MC ALS)
1Hz	-0.45	-28.3dB 0.0dB 0.0dB	-180.0	-23deg -180deg 180deg
10Hz	-0.45	-1.9dB 0.0dB 0.0dB	-180.0	-127deg -180deg 180deg
100Hz	-0.45	0.0dB 0.0dB 0.0dB	-180.0	-174deg -180deg 180deg
1kHz	-0.46	0.0dB 0.0dB 0.0dB	-180.0	-180deg -180deg 180deg
10kHz	-0.45	0.0dB 0.1dB 0.0dB	-181.0	89deg -177deg 89deg
100kHz	-0.46	0.0dB 3.0dB 0.0dB	-190.0	81deg -170deg 81deg

OUT2/IN2	dB	Nom (CM MC ALS)	deg	Nom (CM MC ALS)
1Hz	-0.27	0.0dB 0.0dB 0.0dB	-180.0	180deg 180deg 180deg
10Hz	-0.27	0.0dB 0.0dB 0.0dB	-180.0	180deg 180deg 180deg
100Hz	-0.27	0.0dB 0.0dB 0.0dB	-180.0	180deg 180deg 180deg
1kHz	-0.27	0.0dB 0.0dB 0.0dB	-180.0	180deg 180deg 180deg
10kHz	-0.27	0.0dB 0.0dB 0.0dB	-180.3	180deg 180deg 180deg
100kHz	-0.27	0.0dB 0.0dB 0.0dB	-183.4	177deg 177deg 177deg

Transfer Functions of Boost Gain Stages (SR785BoostGainTFs.bat)

Type SR785BoostGainTFs and Press Enter.

Note: 1. Switch D5 must be **Down** (low) for **all** measurements.
 2. All other switches are in default unless prompted otherwise
 3. If DC Bias is not properly adjusted, these tests will fail.

It is also possible to measure these boost stages by using TP3, TP8, TP9, TP10 and TP11A. See Appendix A4 for typical examples.

** Tolerances must be within 1dB and 5deg of nominal.

Boost #	@10Hz	Nom	@100Hz	Nom	@1kHz	Nom
Reference	-32.42@	-181.1	-32.67@	192.14	-39.9@ -2	45.36
Common Comp. (D17)	39.45 -14.63	39.7dB -14deg	31.13 -67.59	31.4dB -67deg	11.98 -73.36	12.3dB -74deg
1. (D15)	25.90 -0.50	26.3dB -1deg	25.75 -7.43	26.3dB -5deg	22.0 -46.0	23.4dB -42deg
2. (D16)	25.72 -1.70	26.3dB -1deg	25.60 -3.61	26.3dB -5deg	21.97 -46.0	23.4dB -42deg
3. (D15+D16)	22.65 -1.82	23.5dB -2deg	22.37 -15.31	23.1dB -17deg	12.47 -60.78	12.9dB -61deg
Lift D28 (slow comp)	31.35 -68.54	31.3dB -68deg	12.0 -87.78	11.9dB -88deg	-7.98 -89.7	-8.1dB -90deg
D29 (slow boost)	31.22 -67.29	31.4dB -67deg	12.09 -73.60	12.3dB -74deg	0.51 -21.59	0.6dB -22deg

Transfer Functions of DAQ Channels (SR785DAQTFs.bat)

Return all switches to default positions. Type SR785DAQTFs and Press Enter.

Measure the transfer function from SR785 CH1 A to D0901781 Monitor jack (DAQ channels). Sweep the frequency from 10kHz down to 1Hz at 1mV source amplitude. Record the values at 1Hz and 10kHz in the table below. See Appendix A5 for typical examples.

** Tolerances must be within 1dB and 5deg of nominal.

Frequency	1Hz	Nominal	10kHz	Nominal
IMON	45.72, 0.25	45dB, 0deg	45.01, -1.53	45dB, 0deg
FMON	5.22, -169.33	5dB, 11deg	45.36, -183.8	45dB, -180deg
SMON	5.18, -169.87	5dB, -170deg	45.35, -184	45dB, -180deg

Tests Part 3: 4395A Network/Spectrum Analyzer

Connect the 4395A in a similar fashion to the SR785, with a GPIB to Cat5 adapter.

Note: The model used must be a 4395A. Script was specifically written for the 4395A.

High Frequency Transfer Function (AG4395AHighFreqTF.bat)

Ensure D5 is in the default position.

Type AG4395AHighFreqTF

Use a network analyzer to measure the transfer function from IN1 to SERVO. Sweep the frequency from 10MHz down to 10kHz with -20dBm source. To remove cable delays first measure the transfer function against a BNC barrel and use as a reference. **Record** the displayed values at 100kHz, 300kHz and 1MHz in the table below. Nominal values are given for CM. See Appendix A3 for typical examples.

** Tolerances are within 1dB and 5deg of nominal.

Frequency	SERVO/IN1 [dB]	Nominal	SERVO/IN1 [deg]	Nominal
100kHz	-0.55	0dB	168.36	170deg
300kHz	-0.72	0dB	145.52	150deg
1MHz	-1.98	-2dB	68.85	75deg