

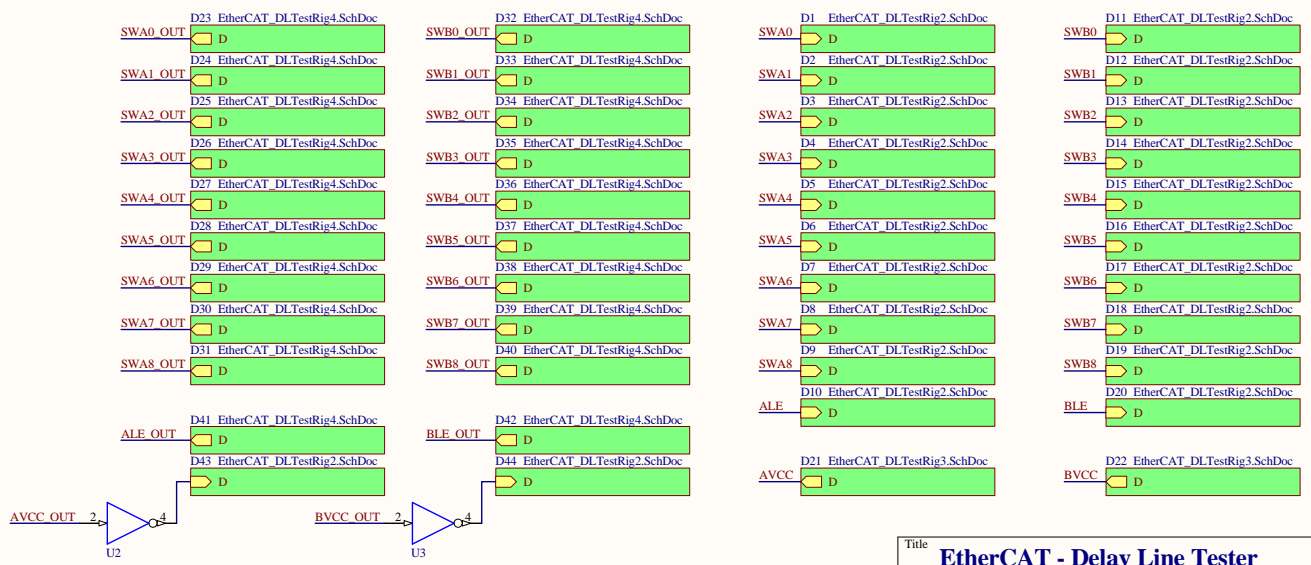
Digital Inputs:
SWA[LE]: Latch enable
SWA[8.. 0]: Delay Line Phase Shifter 1/16, 1/8, 1/4, 1/2, 1, 2, 4, 8, 16 ns
SWB[LE]: Latch enable
SWB[8.. 0]: Delay Line Phase Shifter 1/16, 1/8, 1/4, 1/2, 1, 2, 4, 8, 16 ns

Digital Outputs:
SWA_OUT[LE]: Latch enable
SWA_OUT[8.. 0]: Delay Line Phase Shifter 1/16, 1/8, 1/4, 1/2, 1, 2, 4, 8, 16 ns
SWB_OUT[LE]: Latch enable
SWB_OUT[8.. 0]: Delay Line Phase Shifter 1/16, 1/8, 1/4, 1/2, 1, 2, 4, 8, 16 ns

Analog Outputs:
AVCC: 5V on Delay Line Board
BVCC: 5V on Delay Line Board

Analog Inputs:
AVCC_OUT: 5V from Delay Line Board
BVCC_OUT: 5V from Delay Line Board

- H1 Handle, 3"
- H2 Handle, 3"
- Mouser 534-9109
- H3 Ferrule
- H4 Ferrule
- H5 Ferrule
- H6 Ferrule
- Mouser 534-9121
- M1 #6-32 3/8" flat
- M2 #6-32 3/8" flat
- M3 #6-32 3/8" flat
- M4 #6-32 3/8" flat
- McMaster-Carr 91099A215
- M5 M4-8mm, flat
- M6 M4-8mm, flat
- M7 M4-8mm, flat
- M8 M4-8mm, flat
- M9 M4-8mm, flat
- M10 M4-8mm, flat
- M11 M4-8mm, flat
- M12 M4-8mm, flat
- M13 M4-8mm, flat
- M14 M4-8mm, flat
- M15 M4-8mm, flat
- M16 M4-8mm, flat
- McMaster-Carr 91420A218
- PN2 Front Panel
- LIGO D1100578-v1
- PN3 Rear Panel
- LIGO D1100579-v1
- PN4 DB25 cable
- Newark 05M6812
- PN5 DB25 cable
- Newark 05M6817
- PN6 IDC header
- PN7 IDC header
- PN8 IDC header
- PN9 IDC header
- Newark 73K6217
- H7 Standoff M-F
- H8 Standoff M-F
- H9 Standoff M-F
- H10 Standoff M-F
- H11 Standoff M-F
- H12 Standoff M-F
- H13 Standoff M-F
- H14 Standoff M-F
- H15 Standoff M-F
- H16 Standoff M-F
- H17 Standoff M-F
- H18 Standoff M-F
- Newark 56K3066
- PN10 Banana jack
- Newark 39F1574
- PN11 Banana jack
- Newark 39F1575
- PN12 Jack screw
- PN13 Jack screw
- PN14 Jack screw
- PN15 Jack screw
- Newark 80K5431
- H19 Standoff F-F
- H20 Standoff F-F
- H21 Standoff F-F
- H22 Standoff F-F
- H23 Standoff F-F
- H24 Standoff F-F
- Newark 56K3039



Title EtherCAT - Delay Line Tester		
Size B	Number D1100472	Revision 1
Date:	4/1/2011	Sheet 1 of 4
File:	C:\Users\...EtherCAT_DLTestRig1.SchDoc; Drawn By: F. Clara	

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A

A

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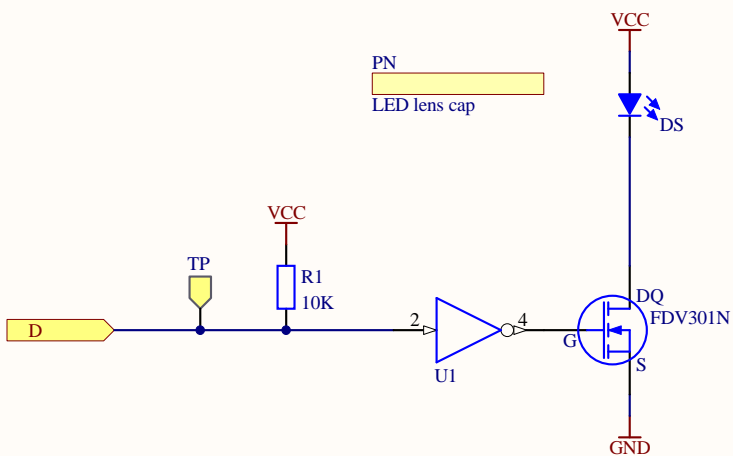
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Title		
EtherCAT - Delay Line Tester		
Size	Number	Revision
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Date:	4/1/2011	Sheet 2 of 4
File:	C:\Users\...\EtherCAT_DLTestRig2.SchDoc Drawn By: F. Clara	

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A

A

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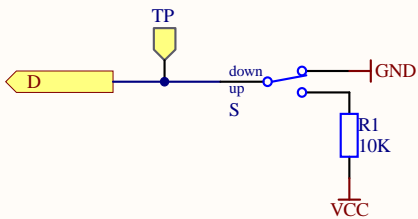
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Title		
EtherCAT - Delay Line Tester		
Size	Number	Revision
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A

A

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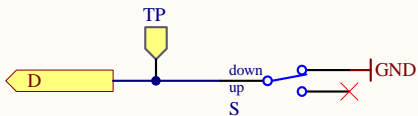
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D



Title		
EtherCAT - Delay Line Tester		
Size	Number	Revision
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Date:	4/1/2011	Sheet 3 of 4
File:	C:\Users\...\EtherCAT_DLTestRig4.SchDoc Drawn By: F Clara	

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