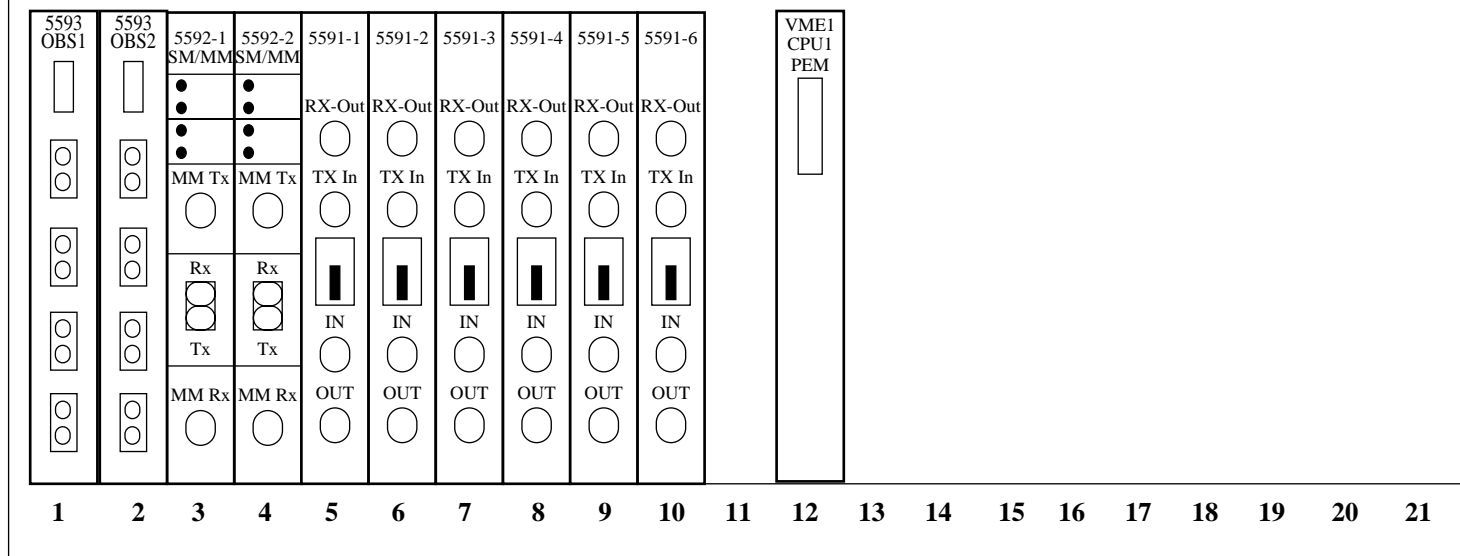
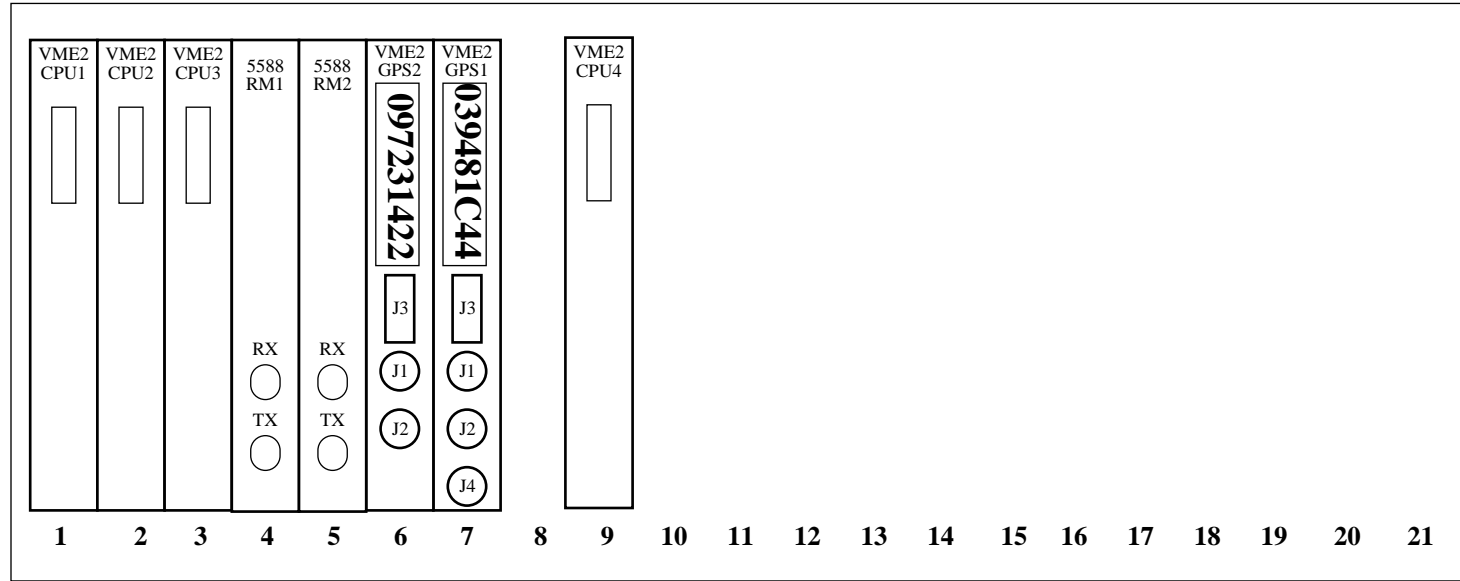


				UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: FRACTIONAL ± ANGULAR RACH ± BEND ± TWO PLACE DECIMAL ± THREE PLACE DECIMAL ± FINISHED SURFACE RMS BREAK CORNERS IN OUT, REMOVE ALL BURRS				CURRENT REVISION APPROVAL		LIGO CALIFORNIA INSTITUTE OF TECHNOLOGY MASSACHUSETTS INSTITUTE OF TECHNOLOGY	
				DO NOT SCALE THIS DRAWING				DRAWN	GROUP	SIGNATURE	DATE
				USED ON:		REV		CHECKED	R. Bork		01/21/00
REFERENCE DRAWINGS				NEXT ASS'Y:		DESCRIPTION		SHEETS EFFECTED			
DWG. NO.	DESCRIPTION			DWG. NO.	DESCRIPTION						SCALE
6				5							SIZE DWG. NO.
										B D990084-A-C	
										SCALE SHEET	
										1 of 3 STD	
										VER. DT	

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DAQS Network Optical Bypass VME Crate



DAQS Controller VME Crate

VME Modules

Slot	Description	Vendor	Model	Designator
MSR-4 VME-1				
01	4 Ch. Optical Bypass Switch	VMIC	5593	OBS-1
02	4 Ch. Optical Bypass Switch	VMIC	5593	OBS-2
03	Single Mode to Multi-Mode Convertor	VMIC	5592	SMMM-1
04	Single Mode to Multi-Mode Convertor	VMIC	5592	SMMM-2
05	Optical Bypass Switch	VMIC	5591	5591-1
06	Optical Bypass Switch	VMIC	5591	5591-2
07	Optical Bypass Switch	VMIC	5591	5591-3
08	Optical Bypass Switch	VMIC	5591	5591-4
09	Optical Bypass Switch	VMIC	5591	5591-5
10	Optical Bypass Switch	VMIC	5591	5591-6
11				
12	MVME-162 (PEM Processor)	Motorola	162-333	CPU-1
MSR-4 VME-2				
01	MIPS CPU (DAQS Controller)	Artesyn	Baja4700	CPU-1
02	MIPS CPU (DAQS Config. Manager)	Artesyn	Baja4700	CPU-2
03	MIPS CPU (RFM Switch)	Artesyn	Baja4700	CPU-3
04	Reflected Memory (DAQS Network)	VMIC	5588DMA	RM-1
05	Reflected Memory (FrameBuilder Net)	VMIC	5588DMA	RM-2
06	GPS Time Reciver (Time Master)	Brandywine		GPS-2
07	GPS Time Receiver (Clock Master)	Brandywine		GPS-1
09	MIPS CPU (GPS TP Manager)	Artesyn	Baja4700	CPU-3

UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES:
FRACTIONAL ±
ANGULAR RACH ± BEND ±
TWO PLACE DECIMAL ±
THREE PLACE DECIMAL ±
FINISHED SURFACE RMS
BREAK CORNERS IN OUT,
REMOVE ALL BURRS

DO NOT SCALE THIS DRAWING
USED ON:
NEXT ASS'Y:

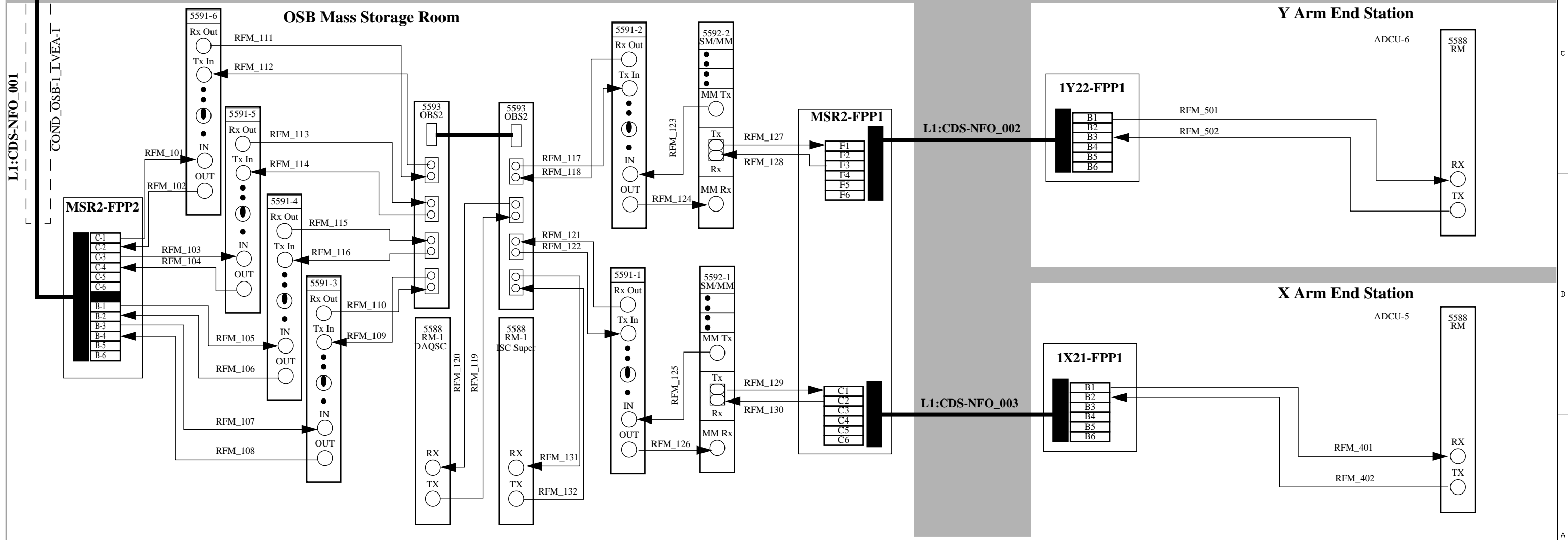
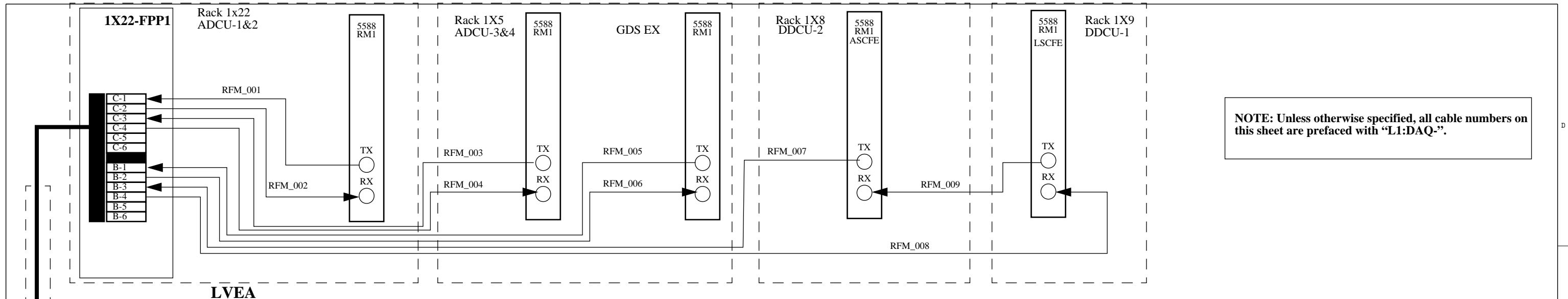
CURRENT REVISION APPROVAL

DRAWN: R. Bork
GROUP: R. Bork
SIGNATURE: [Signature]
DATE: 1/21/00

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Livingston DAQS Network Connections
Controller / Main Hub VME Crate Layout

DWG. NO.	DESCRIPTION	DWG. NO.	DESCRIPTION	REV	DESCRIPTION	SHEETS EFFECTED	DATE	SCALE	SIZE	DWG. NO.	REV.
6	REFERENCE DRAWINGS	5			ISSUE DESCRIPTION				B	D990084-A-C	
										2 of 3	STD



				UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: FRACTIONAL ± ANGULAR ± BEND ± TWO PLACE DECIMAL ± THREE PLACE DECIMAL ± FINISHED SURFACE RMS BREAK CORNERS ON OUT. REMOVE ALL BURRS				CURRENT REVISION APPROVAL			
				DO NOT SCALE THIS DRAWING				DRAWN GROUP R. Bork SIGNATURE DATE 1/21/00			
DWG. NO. DESCRIPTION DWG. NO. DESCRIPTION				REV. DESCRIPTION SHEETS EFFECTED DATE				LIGO CALIFORNIA INSTITUTE OF TECHNOLOGY MASSACHUSETTS INSTITUTE OF TECHNOLOGY			
REFERENCE DRAWINGS				NEXT ASS'Y:				Livingston DAQS Network Connections 4K IFO Ring Topology			
								SCALE SIZE DWG. NO. D990084-A-C REV.			
								CAB FILE SCALE SHEET 3 of 3 STD VER. 01			

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