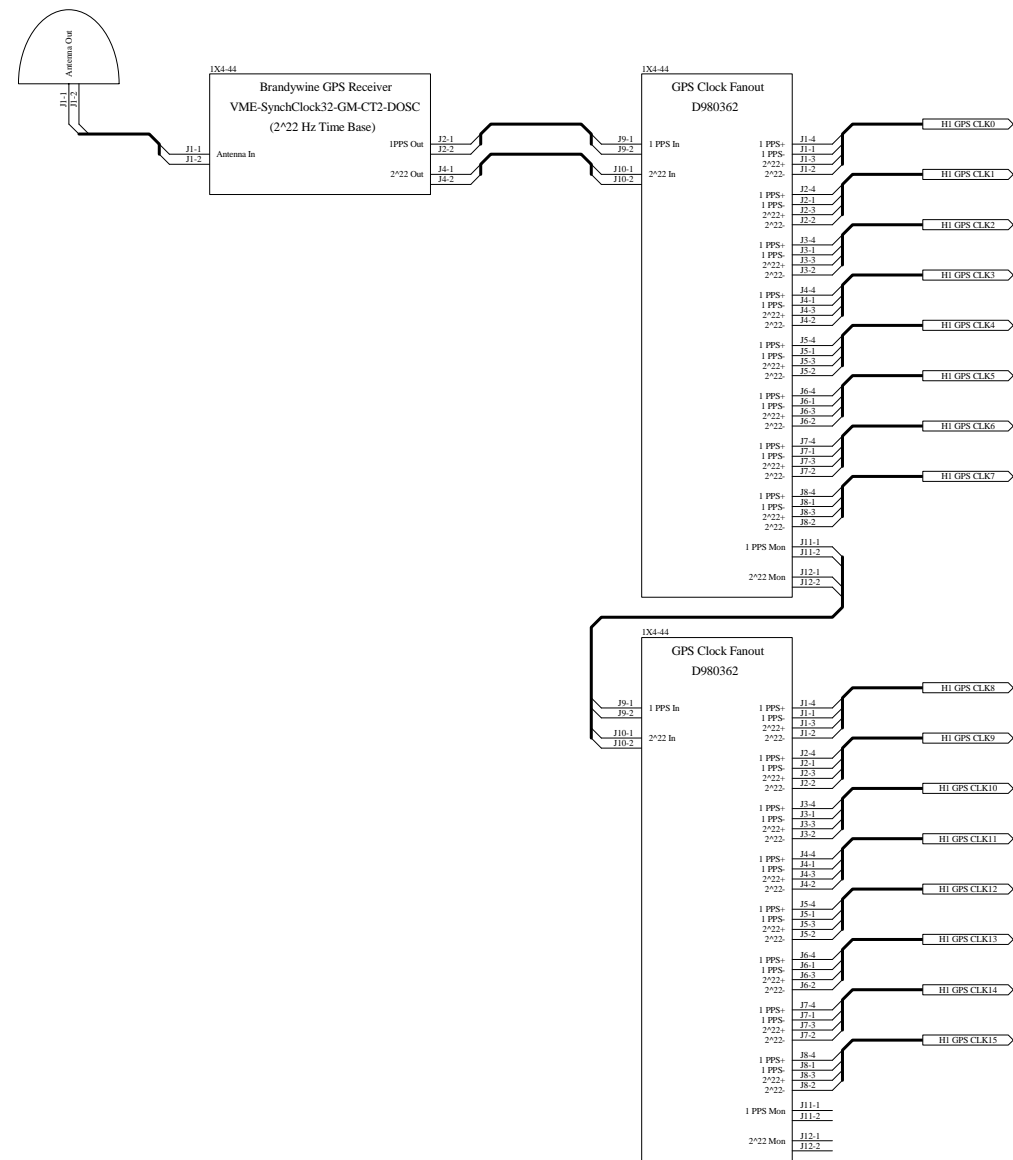
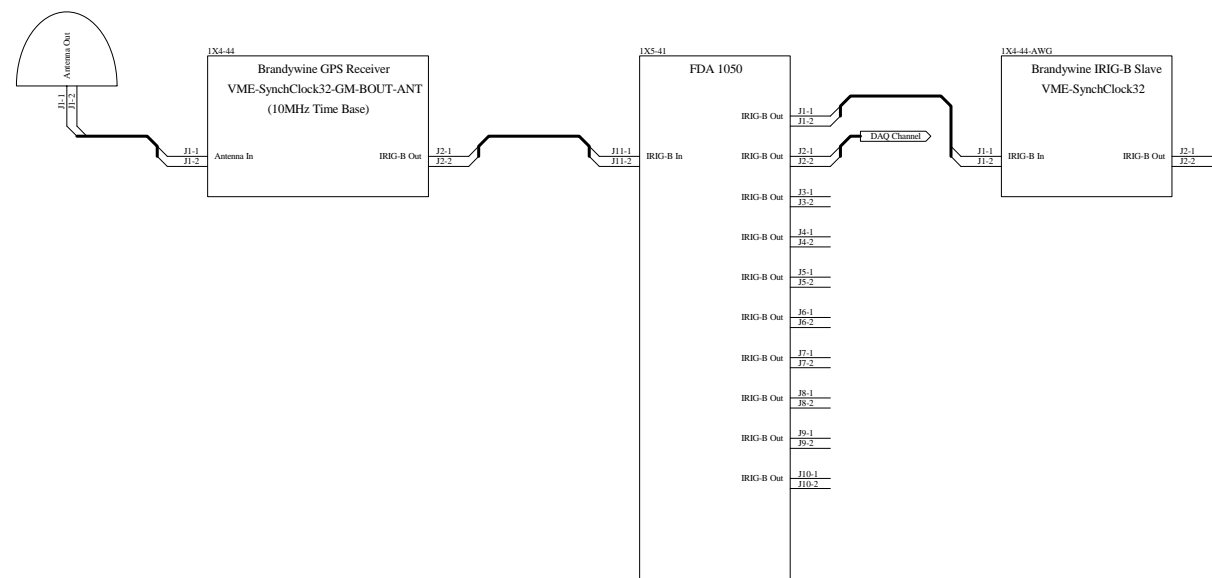


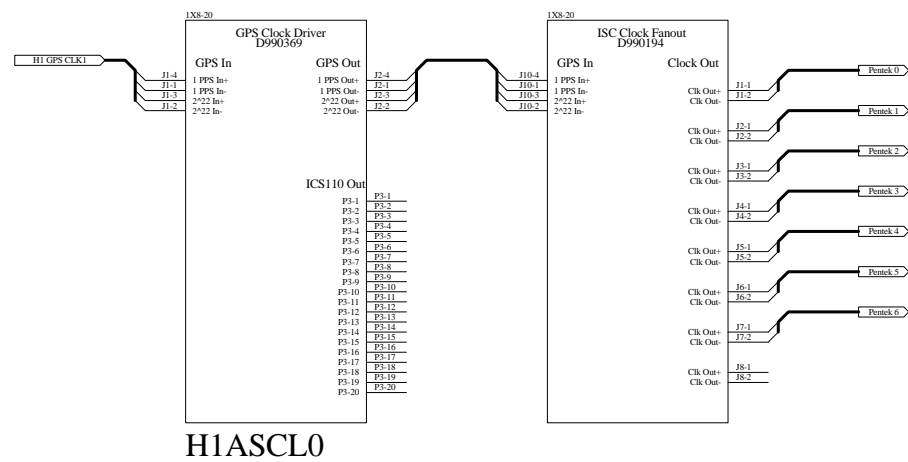
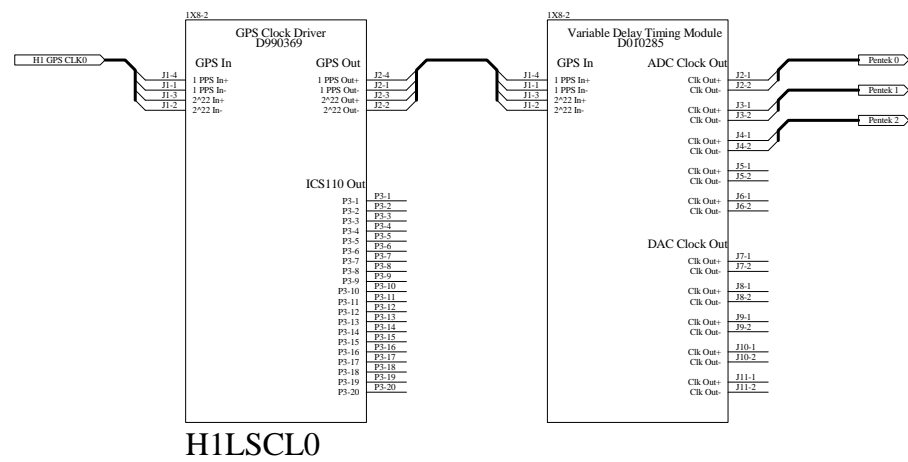
MSR

Rev.	Description	Date
00	Initial release of drawing.	10/7/1999
A	Updated to as-built before fiber based timing system installation.	10/2/2006

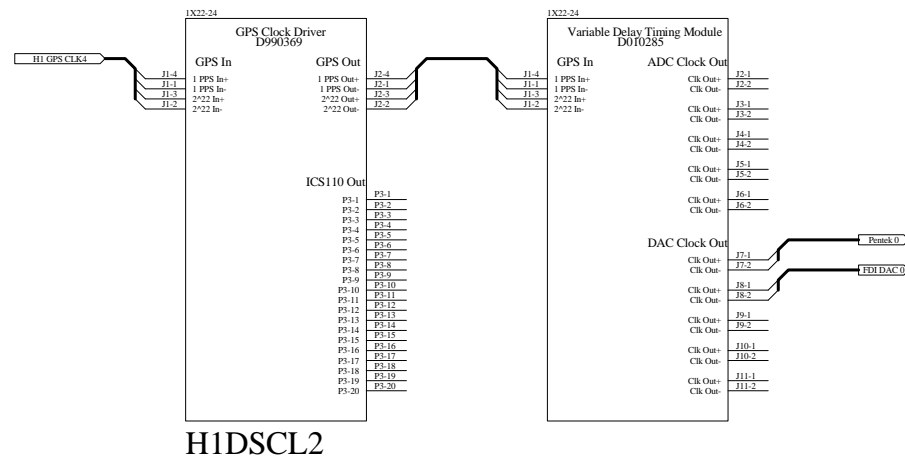
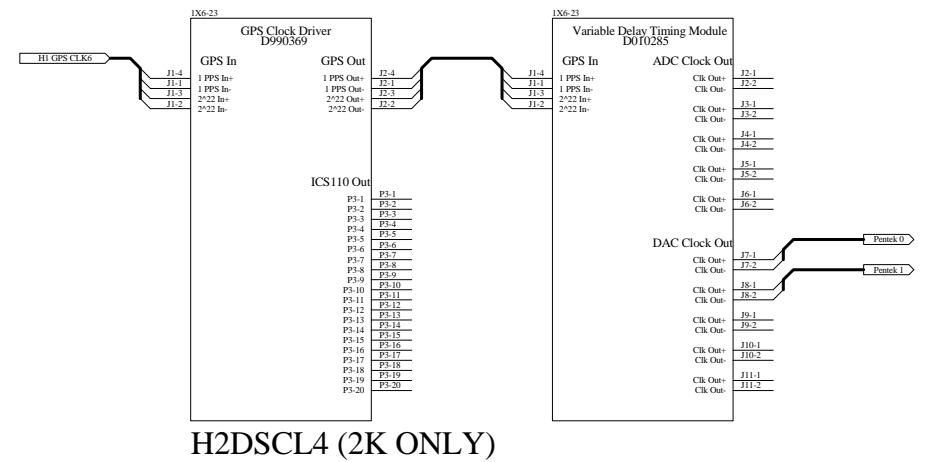
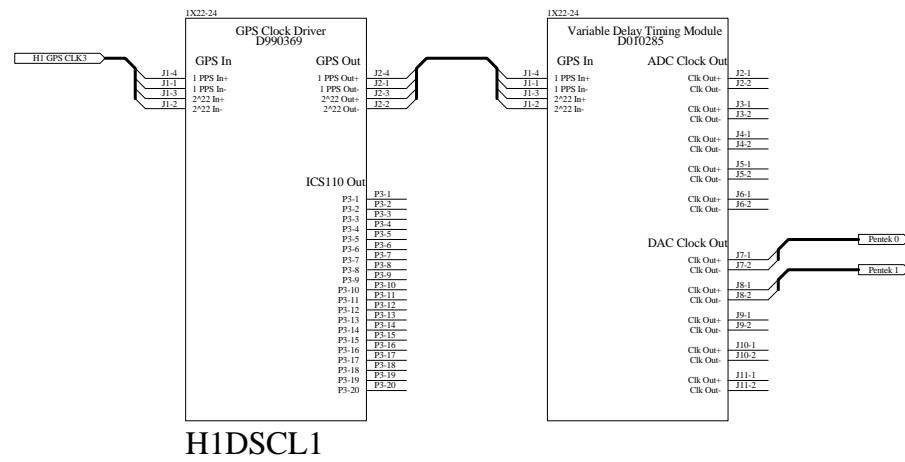
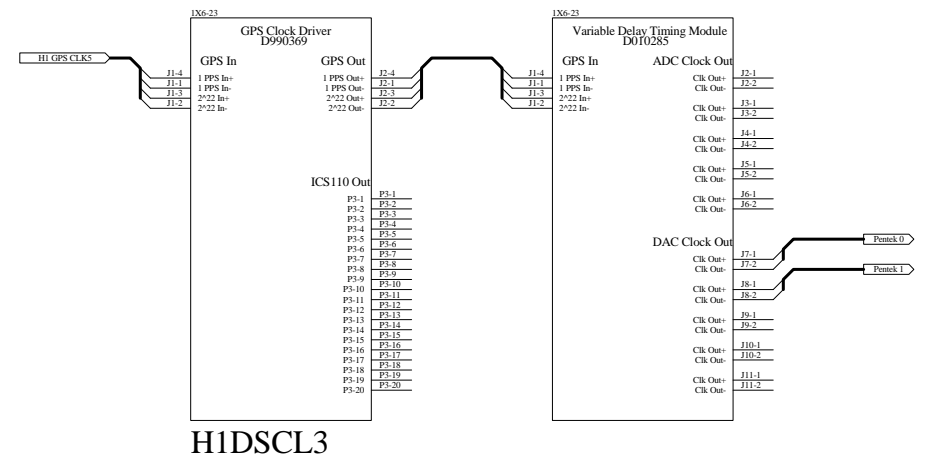
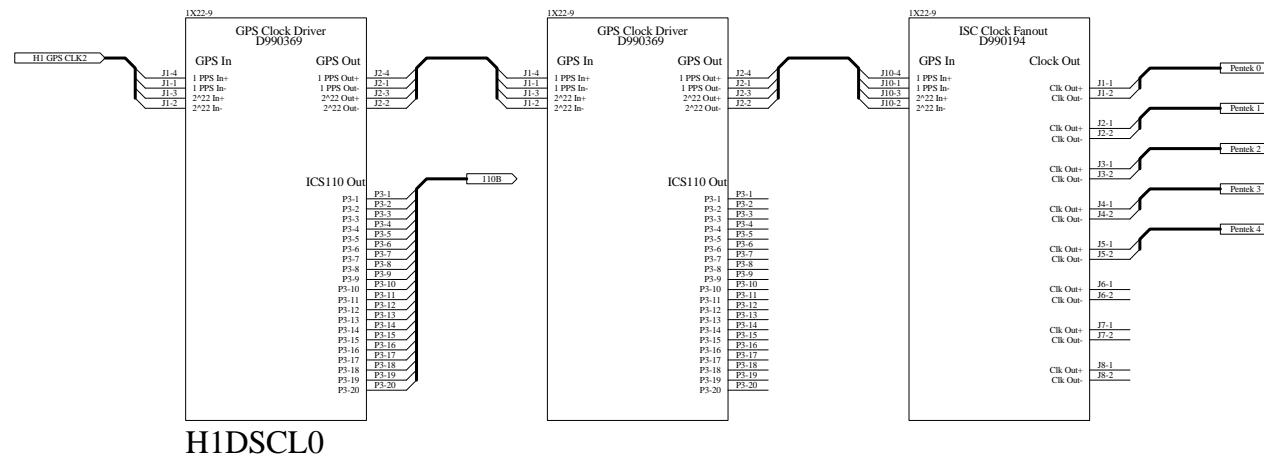
Title: Hanford Timing System Cable Diagram		LIGO Laboratory California Institute of Technology Massachusetts Institute of Technology	LIGO [®]
Size: D	DCC Number: D980354-A-C	SC1: PCB Revision: A	Date: 10/2/2006
File: C:\Documents and Settings\jmyers\My Documents\LIGO\Research\SystemTiming\SystemRev_A\D980354-A0-SCHMS		Author: <i>John R. Myers</i> Time: 7:41:28 AM	Sheet 1 of 6



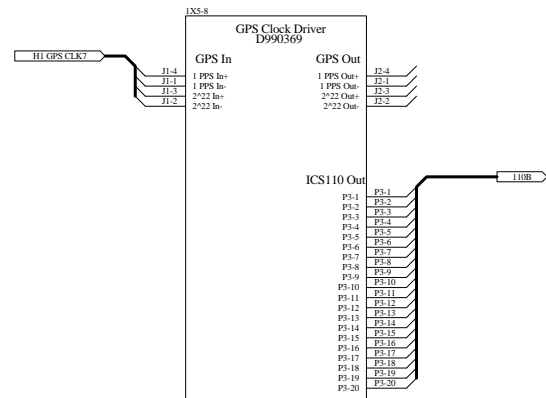
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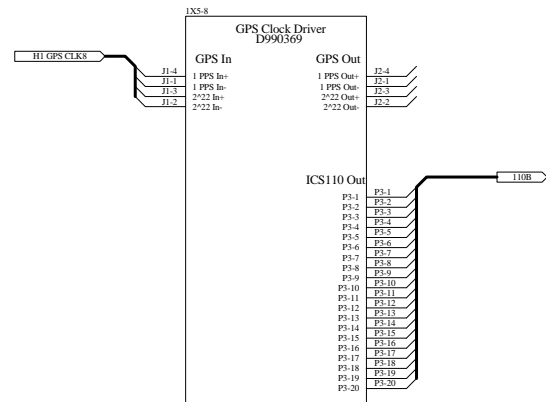
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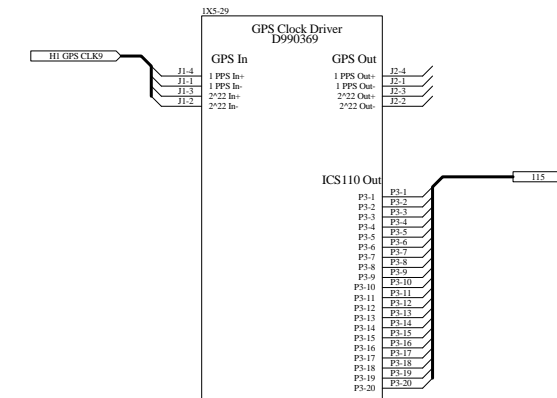
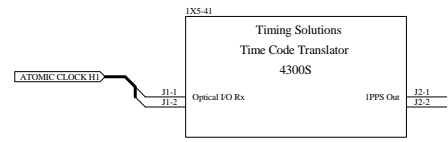
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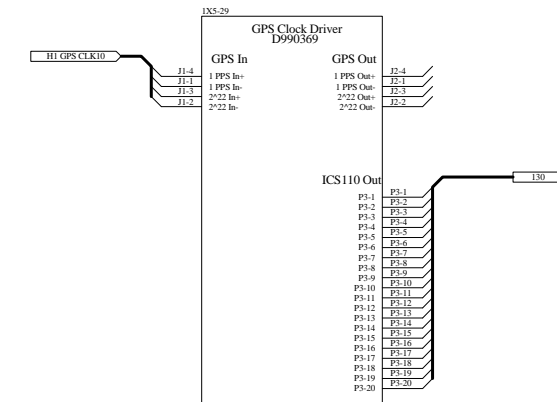
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H1ADCUSUS

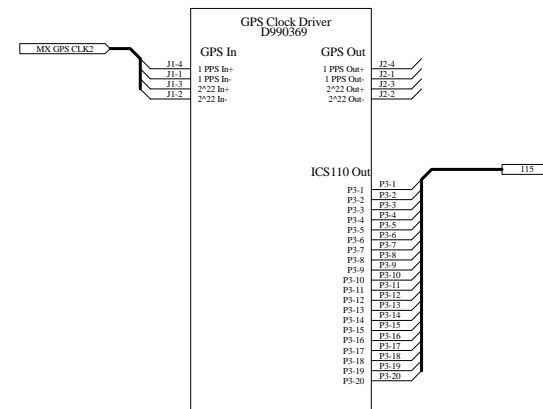
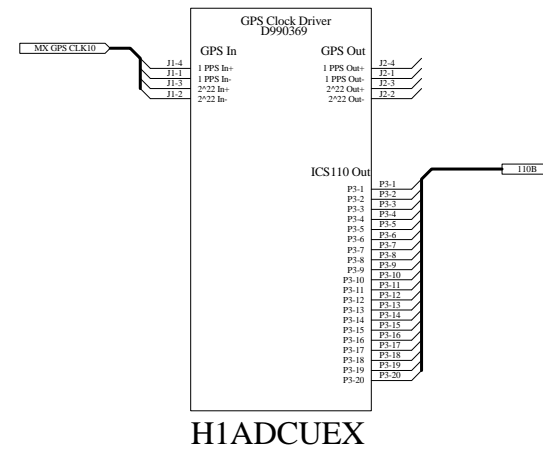
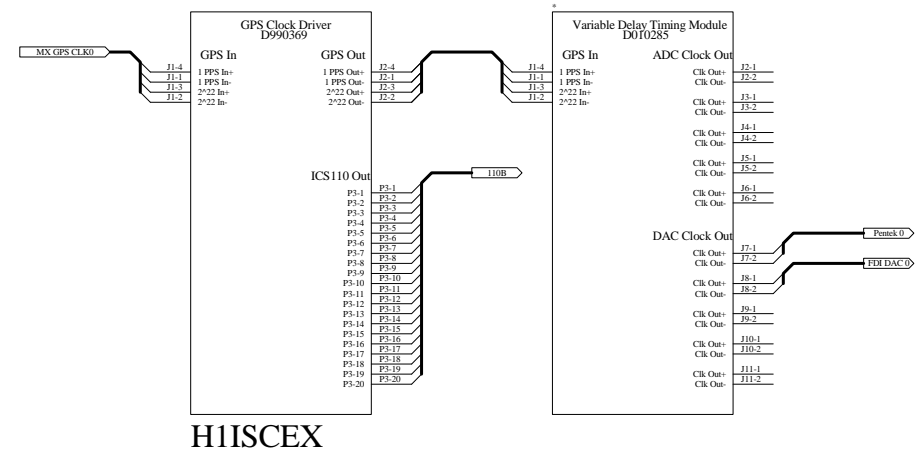
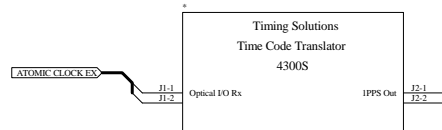
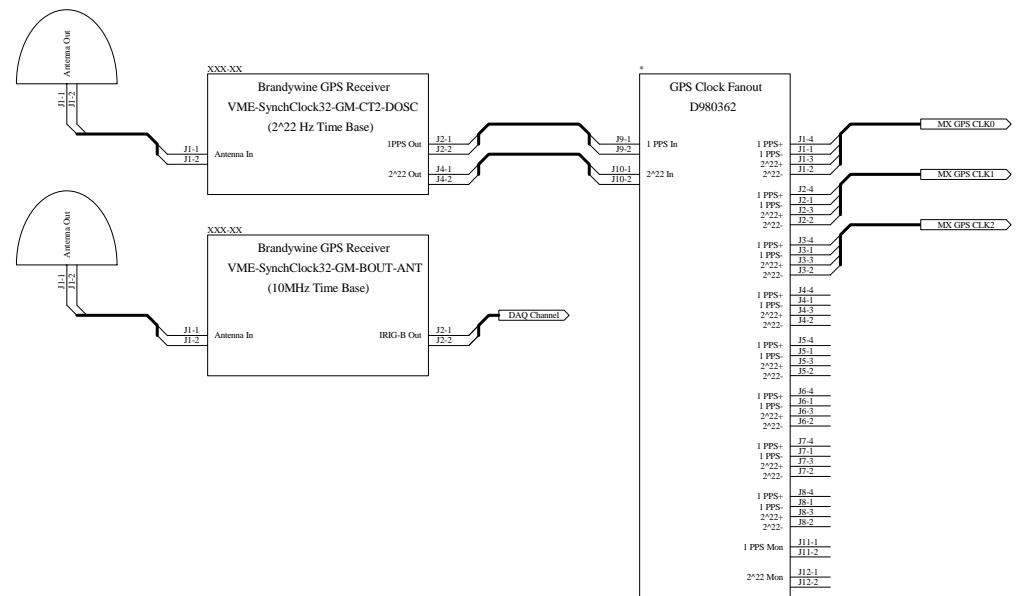


H1AWG



H1ADCUFAST

LVEA



EX