

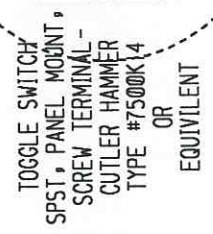
#16/16C 600V, 60 DEG. C SO BELDEN #9427 OR EQUIVALENT

SEE DETAIL "A" (TYPICAL)

INDICATES CHANGE FROM PREVIOUS ISSUE

THIS DETAIL IS TYPICAL FOR ALL PC 'B', BOARD 'B1' OUTPUT TERMINALS SUCH AS: (17, 18) (19, 20) (21, 22) (23, 24) (25, 26) (27, 28) (29, 30) (31, 32) (33, 34) (35, 36) (37, 38) (39, 40) (43, 44) (45, 46) & BOARD 'B2' OUTPUT TERMINALS: (1, 2) (3, 4) (5, 6) (7, 8) (9, 10) AND (11, 12)

MOUNT ALL OVERRIDE SWITCHES (20) IN 1/2" DIA. HOLES ON FRON OF D I/O JUNCTION/CARD PANEL AND WIRE PER NOTE #5.



* Route Through D I/O Junction Panel

NOTES:

1. INSTALL IN SKID 'B' PANEL ON DIFFUSION PUMP POWER CABLES ON THE LOAD SIDE OF THE STARTER. (SEE DWG. #252)
2. INSTALL IN CONTROL ROOM D I/O JUNCTION BOX. (SEE DWG. #252)
3. ENERGIZE MOMENTARILY TO TURN OFF CASKET HOOD PUMP SKID (SEE DWGS. #255 & 256)
4. ENERGIZE MOMENTARILY TO TURN ON CASKET HOOD PUMP SKID. (SEE DWGS. #255 & 256)
5. UNLESS MARKED OTHERWISE, ALL SKID JB INTERNAL D I/O CONTROL WIRES ARE TO BE #14/1C STRANDED, TYPE THHN/THWN. THE SKID JB TO COMPONENT WIRES SHOULD BE AT MINIMUM #16 AWG TWISTED PAIR, 300V, 60 C BELDEN #9471 OR EQUIVALENT.
6. WORK THIS DRAWING WITH DWGS. #ER-250, ER-252, ER-254, ER-255, ER-256, ER-259

<p>BY: DWC DATE: 20MAY96</p> <p>CHKD: BS DATE: 20MAY96</p> <p>DATE: 20MAY96</p> <p>BY: DWC DATE: 20MAY96</p> <p>CHKD: BS DATE: 20MAY96</p>		<p>REVISIONS</p> <p>1. MOVE S3B, S4B, LN2 & GAUGE CTR. TERMS, ETC.</p>		<p>REMARKS</p> <p>SECTION TEST SKID D I/O WIRING DIAGRAM FOR SKID "B"</p> <p>LIGO - ER</p>		<p>CUSTOMER'S NO</p> <p>BY DWC CHKD BS DATE 20MAY96</p> <p>PSE</p> <p>ENGINEERING ASSIGNED</p>		<p>CONTRACT NO</p> <p>953571</p> <p>DWG ER-258</p> <p>SHT</p>		<p>SUPPLIER'S / PURCHASER'S ID</p>	
<p>LIGO-D970046-03-B</p>											