

A

A

B

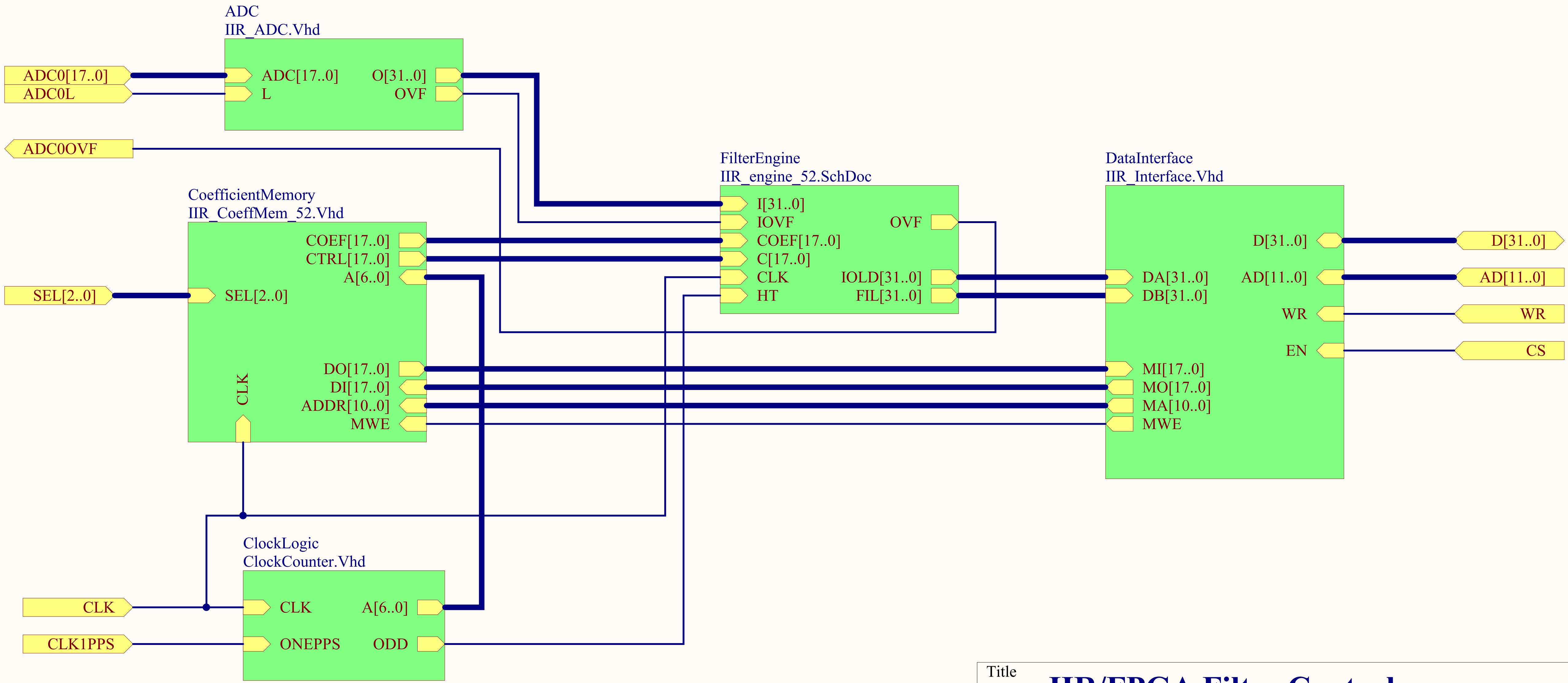
B

C

C

D

D



Title			IIR/FPGA Filter Control	
Size	Number	Revision		
A	D050000-00	0		
Date:	8/28/2007	Sheet 1 of 2		
File:	C:\Users\...\IIR_top_52.SchDoc	Drawn By: Daniel Sigg		

