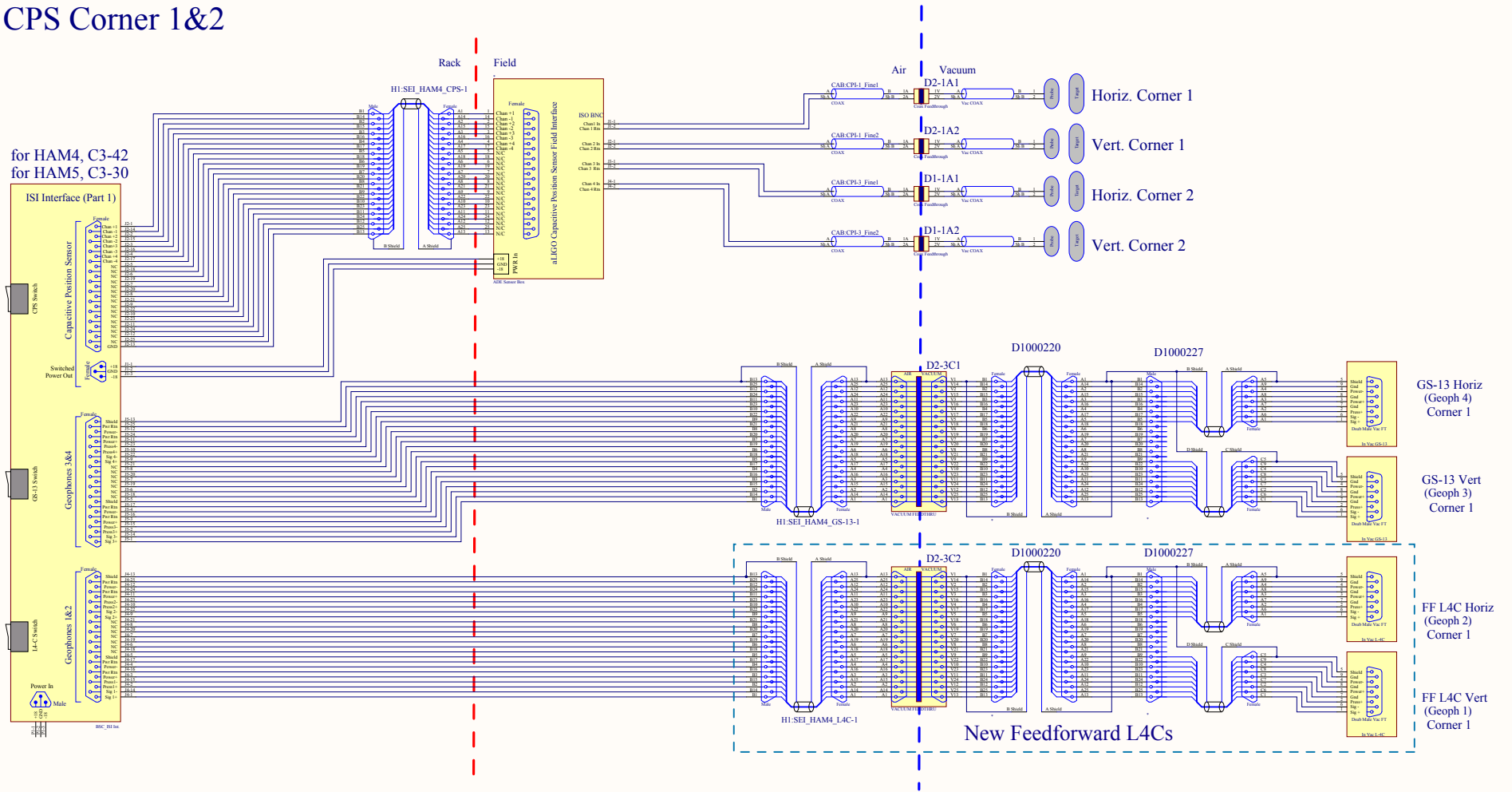


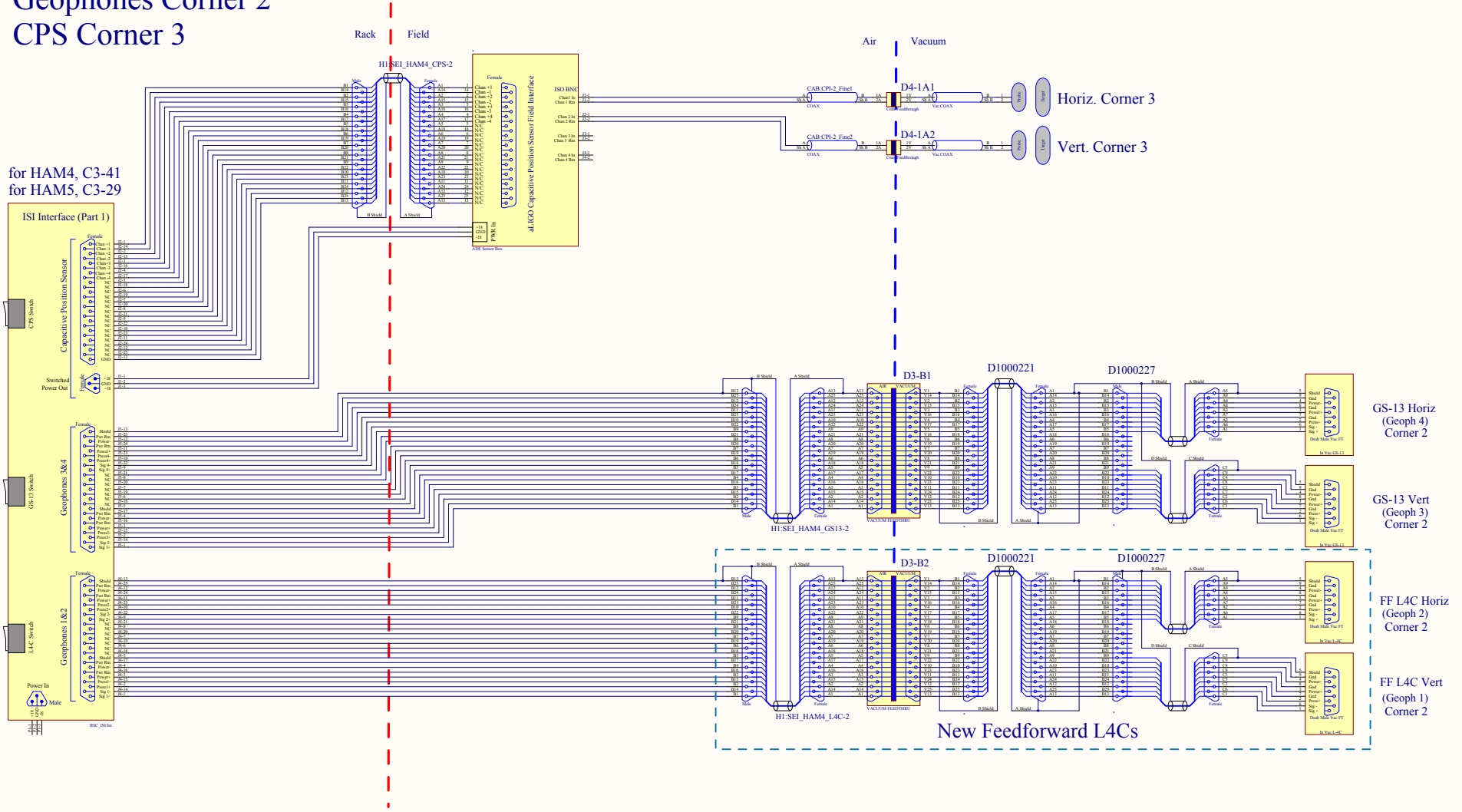
Geophones Corner 1 CPS Corner 1&2

for HAM4, C3-42
for HAM5, C3-30



Geophones Corner 2 CPS Corner 3

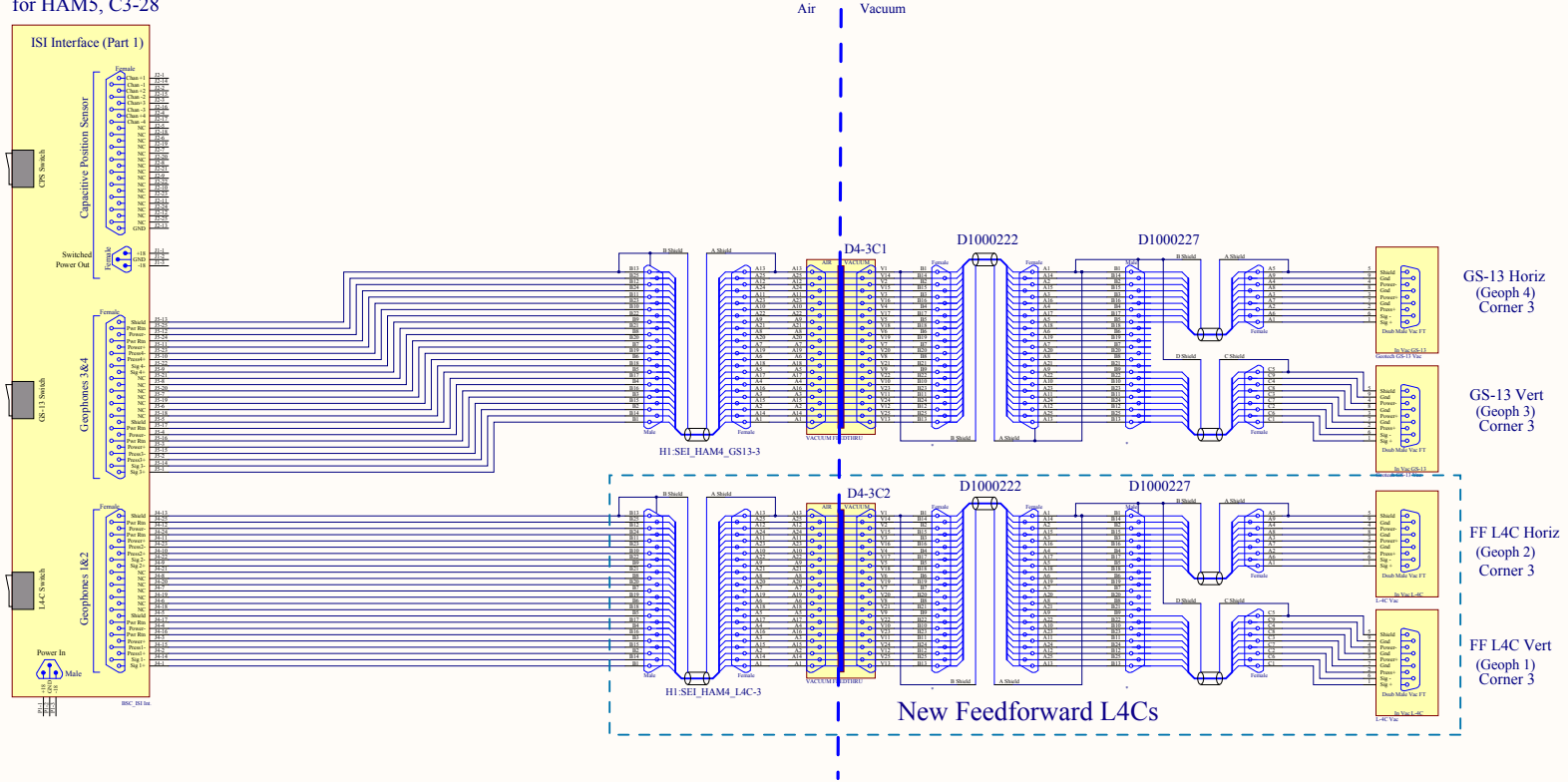
for HAM4, C3-41
for HAM5, C3-29



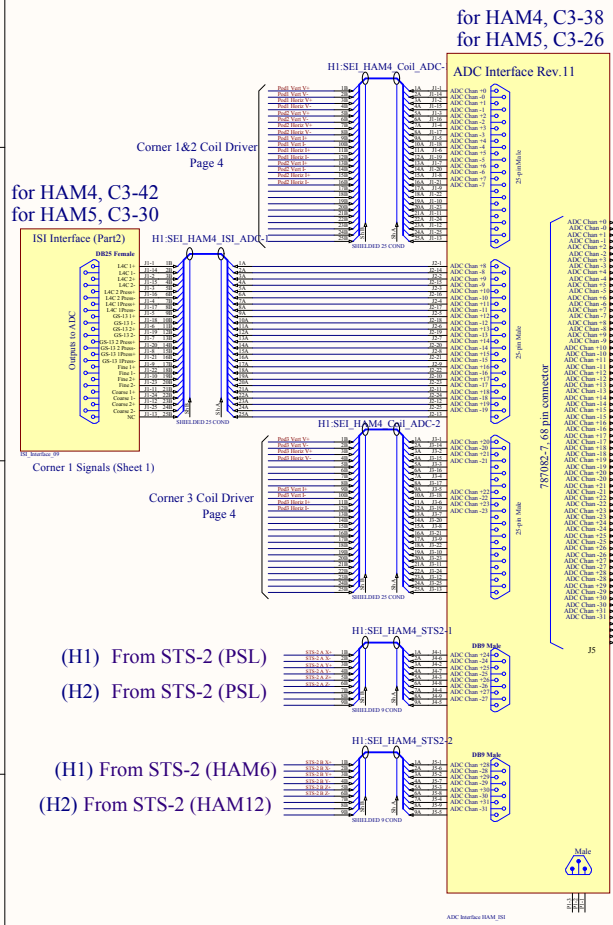
New Feedforward L4Cs

Geophones Corner 3

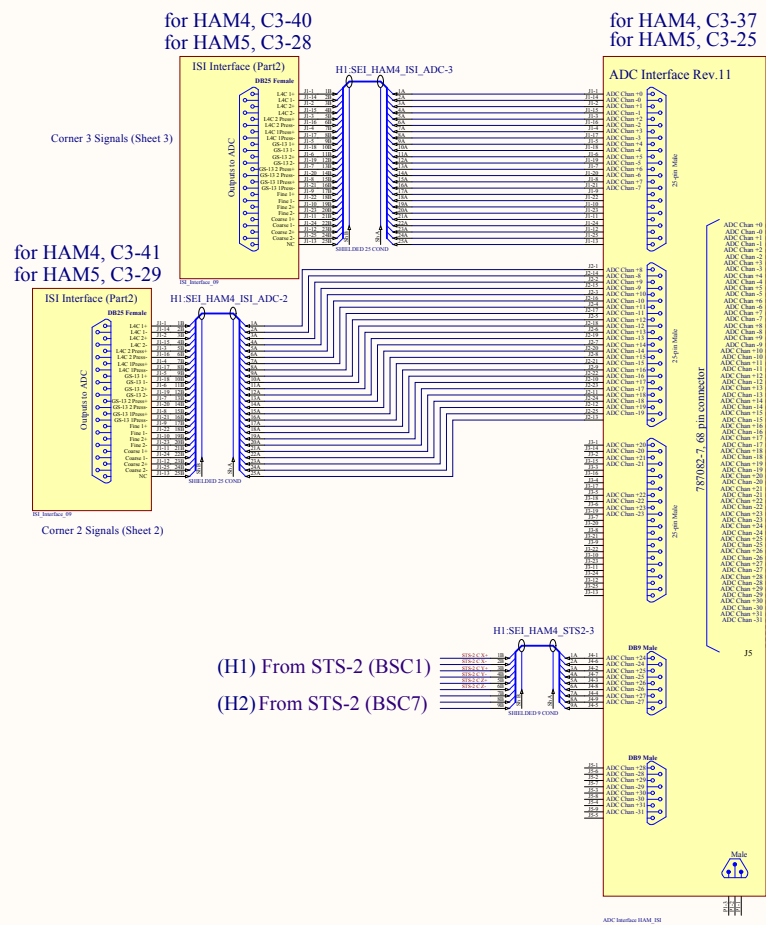
for HAM4, C3-40
for HAM5, C3-28



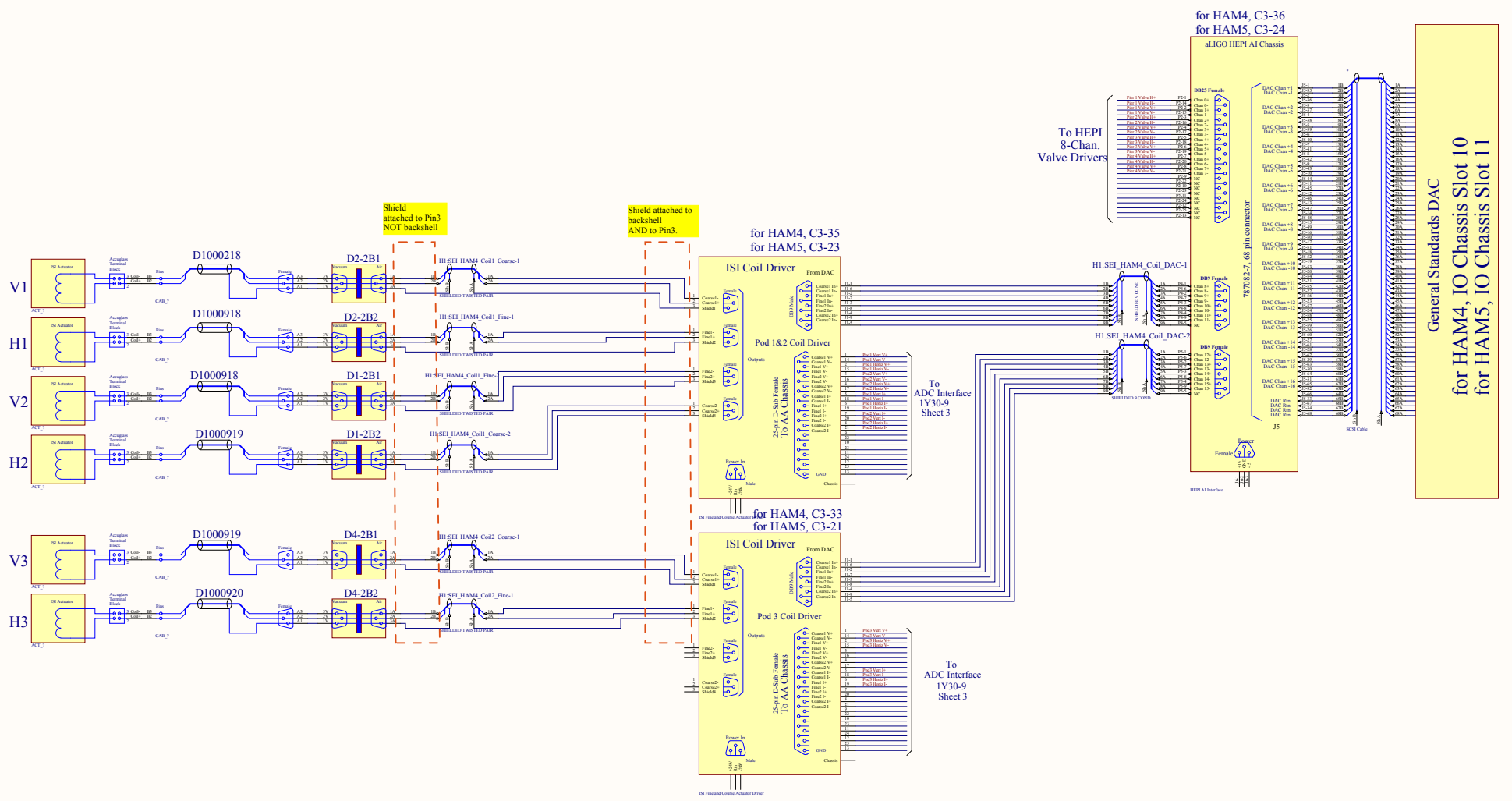
HAMs 4&5 ISI ADCs



General Standards ADC
for HAM4, IO Chassis Slot 6
for HAM5, IO Chassis Slot 8



General Standards ADC
for HAM4, IO Chassis Slot 7
for HAM5, IO Chassis Slot 9



for HAM4, C3-36
for HAM5, C3-24

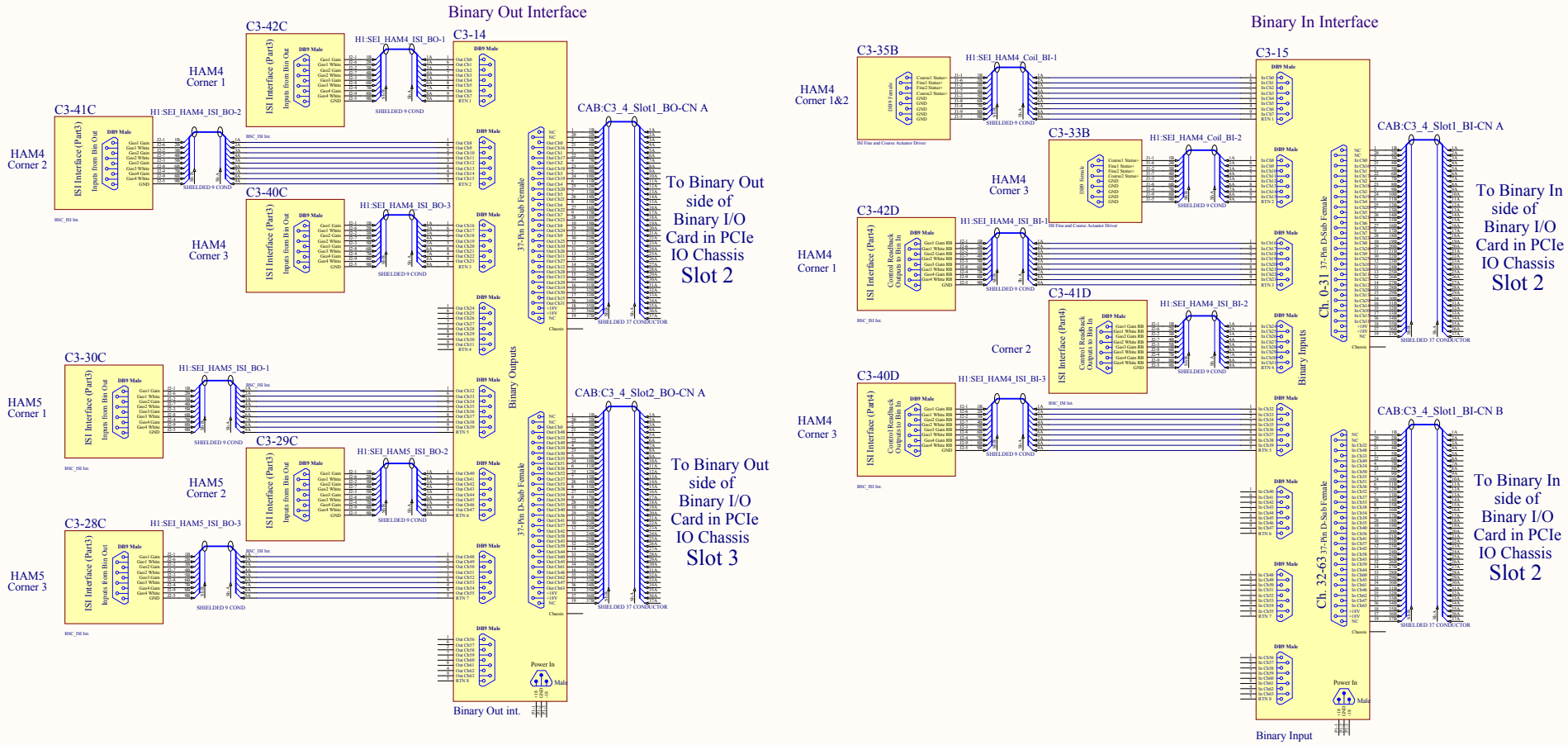
aLIGO HEPI AI Chassis

To HEPI
8-Chan.
Valve Drivers

for HAM4, C3-35
for HAM5, C3-23

for HAM4, C3-33
for HAM5, C3-21

General Standards DAC
for HAM4, IO Chassis Slot 10
for HAM5, IO Chassis Slot 11



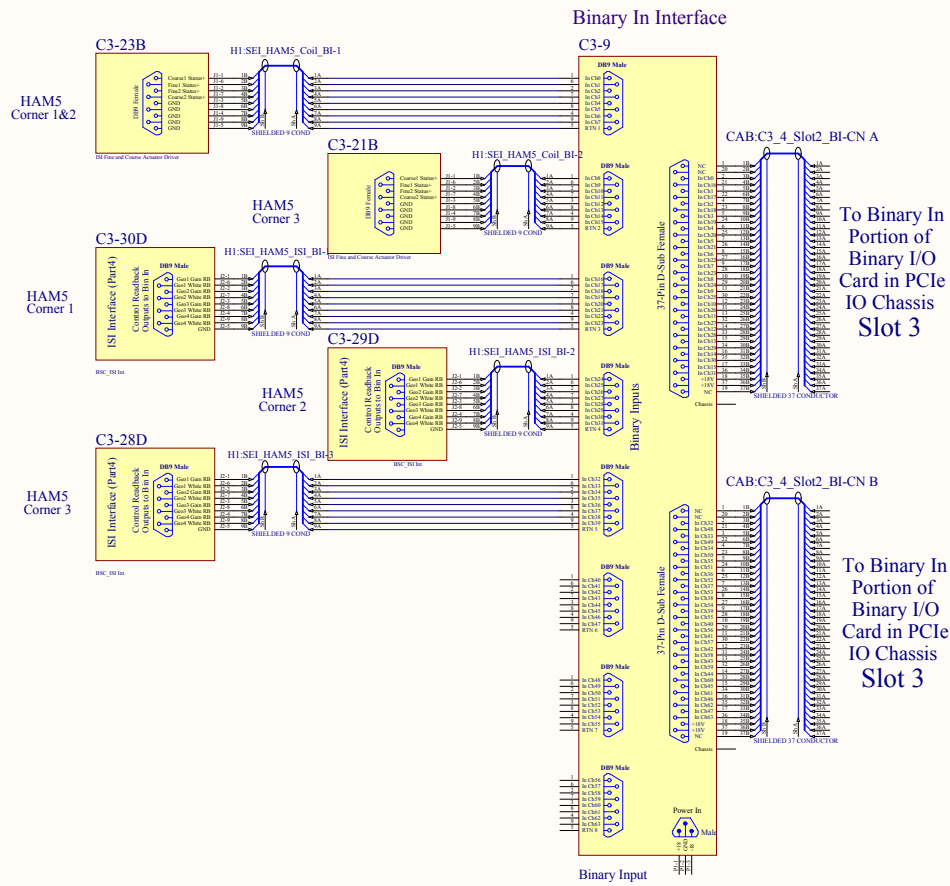
To Binary Out side of Binary I/O Card in PCIe IO Chassis Slot 2

To Binary Out side of Binary I/O Card in PCIe IO Chassis Slot 3

To Binary In side of Binary I/O Card in PCIe IO Chassis Slot 2

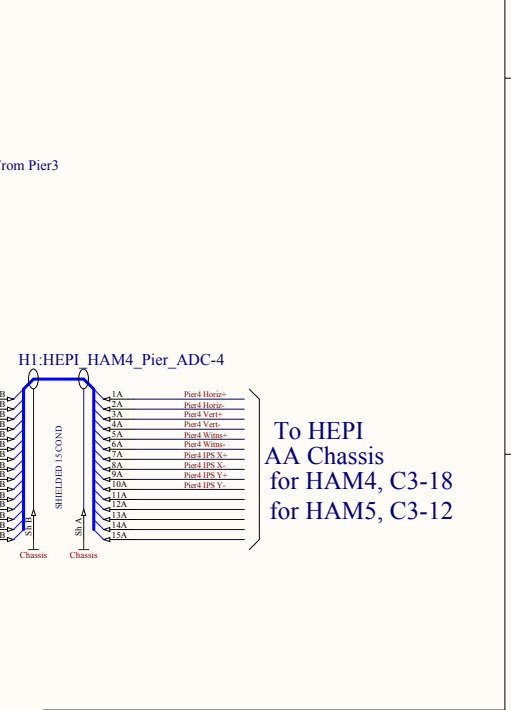
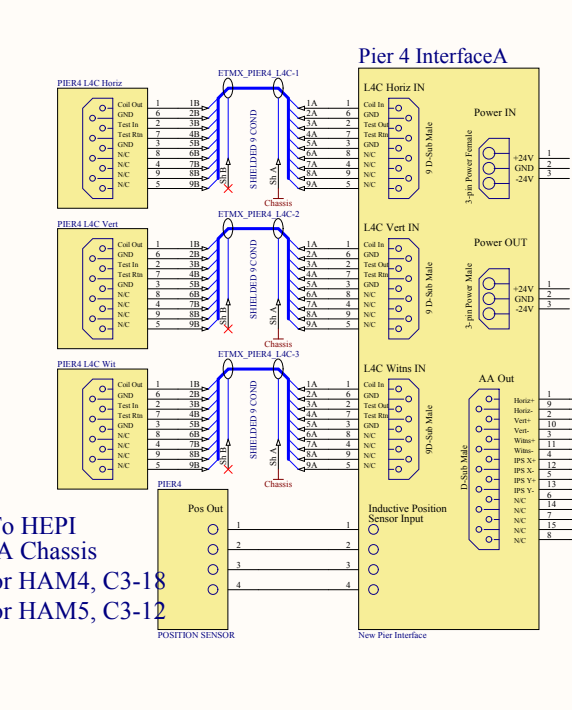
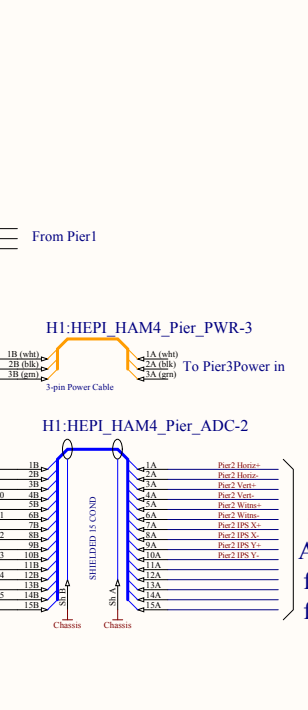
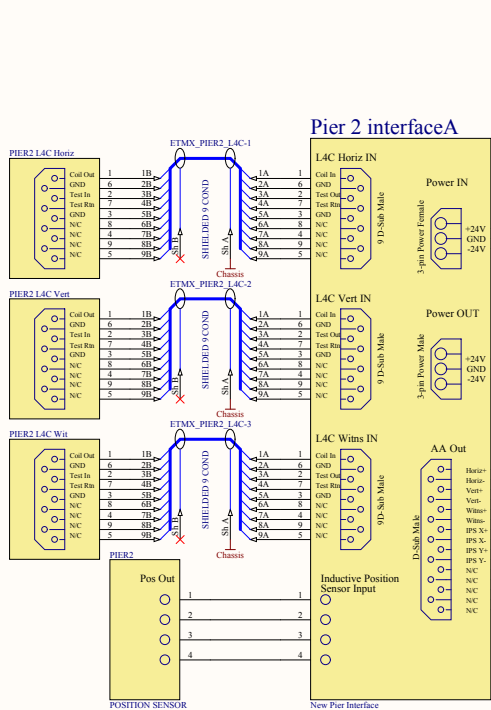
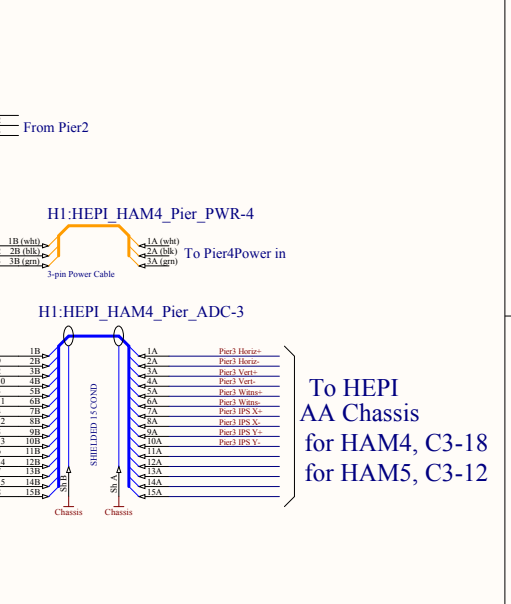
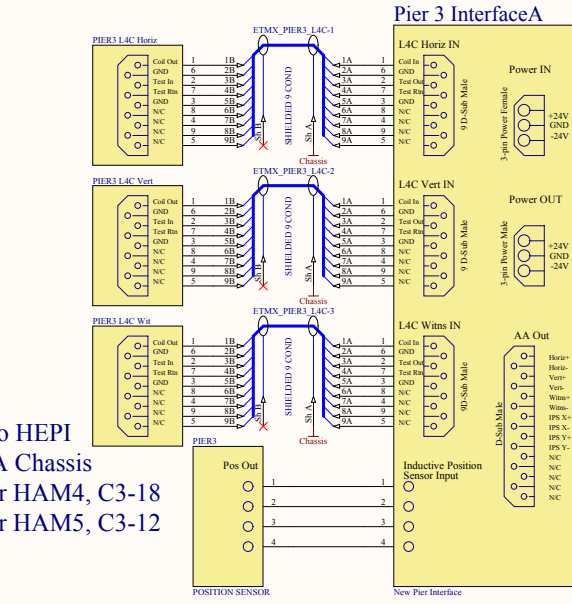
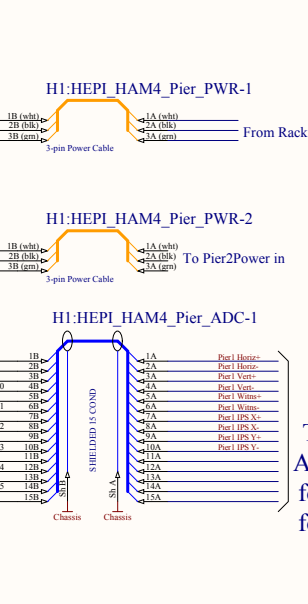
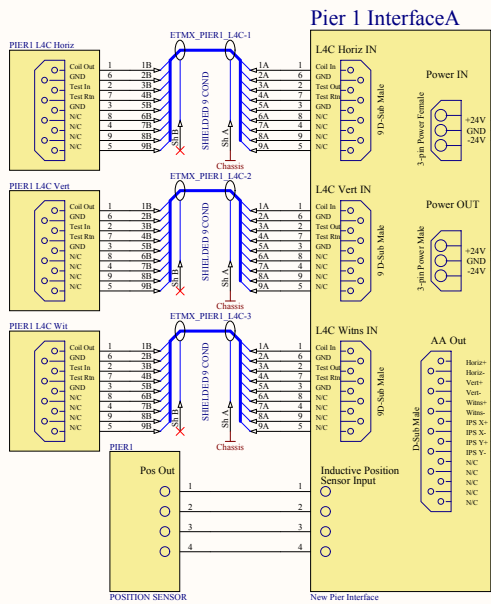
To Binary In side of Binary I/O Card in PCIe IO Chassis Slot 2

ALIGO HAM ISI System Wiring			
Date: Rev: D Issue No: 01080204	Issue No: 01080204 Issue Date: 01/08/2012 Revision: 02	4400 Project ALICE Upgrade ALICE Upgrade ALICE Upgrade	Drawn by: M. Checked by: M. Approved by: M.

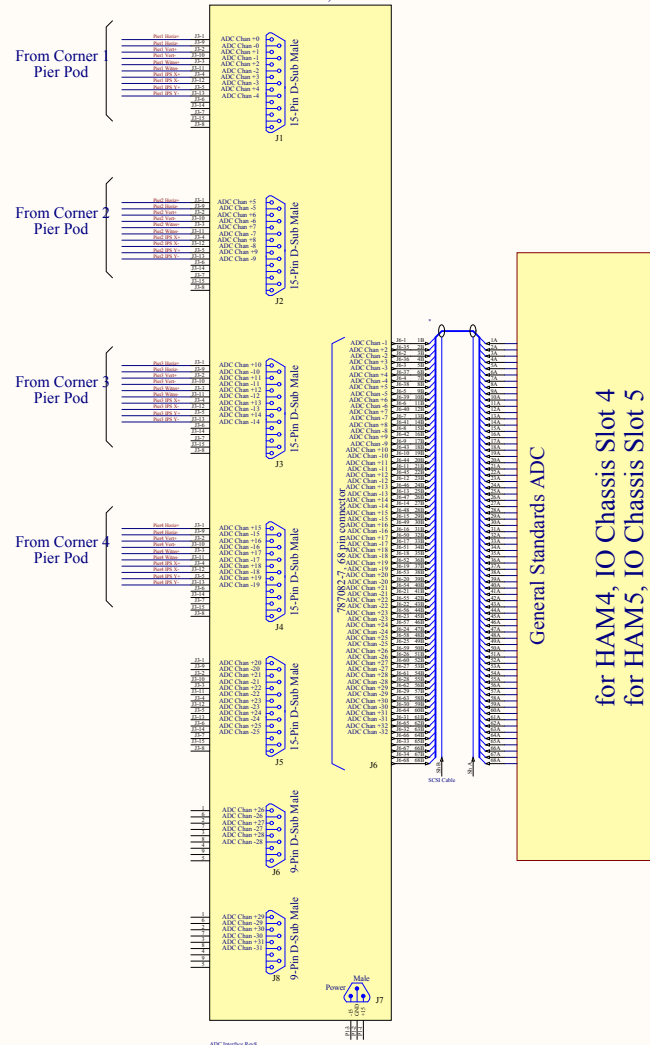


To Binary In Portion of Binary I/O Card in PCIe IO Chassis Slot 3

To Binary In Portion of Binary I/O Card in PCIe IO Chassis Slot 3

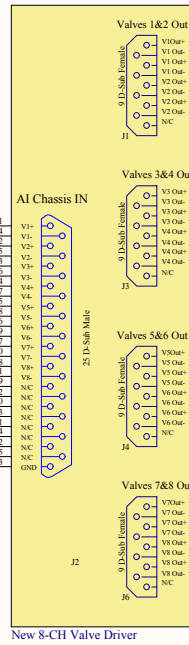


for HAM4, C3-18
for HAM5, C3-12



From AI Chassis
for HAM4, C3-36
for HAM5, C3-24
Page 5

for HAM4, C3-16
for HAM5, C3-10



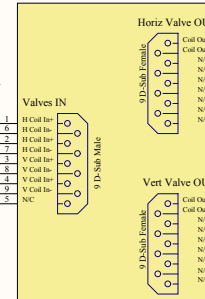
H1:HEPI_HAM4_Valve-1

H1:HEPI_HAM4_Valve-

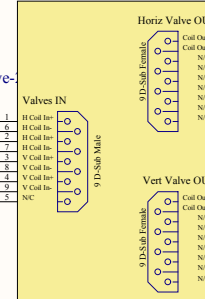
H1:HEPI_HAM4_Valve-

H1:HEPI_HAM4_Valve-4

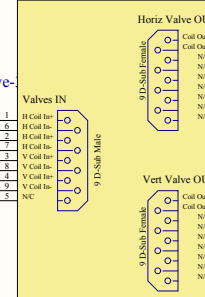
Pier 1 InterfaceB



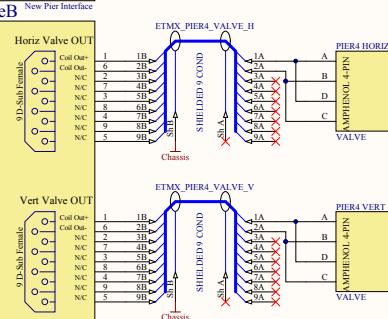
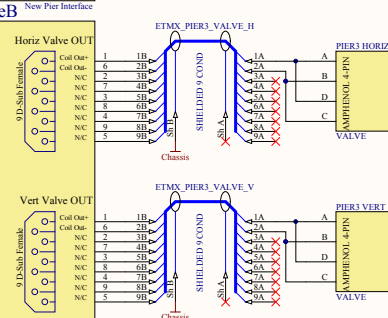
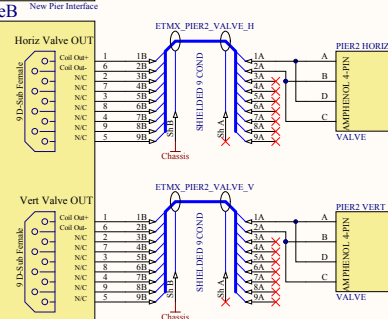
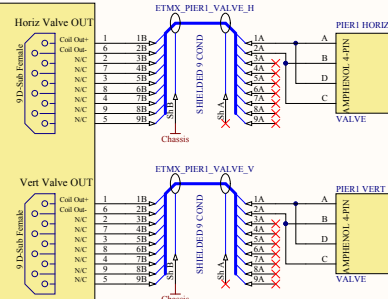
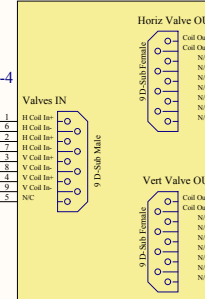
Pier 2 InterfaceB



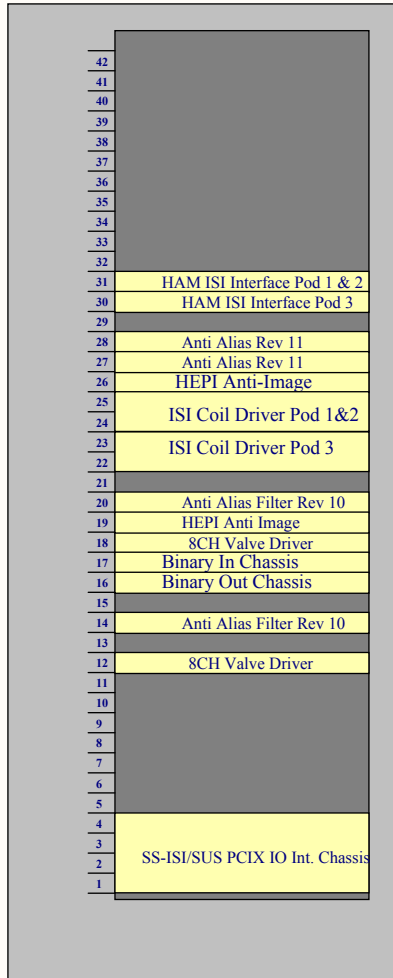
Pier 3 InterfaceB



Pier 4 InterfaceB



HAMs 1&6 L1-SEI-C1



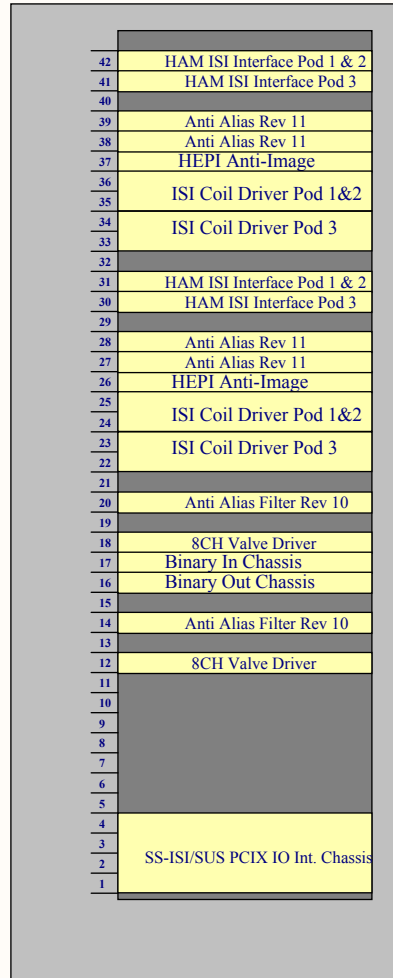
HAM AA Chassis = 10
HEPI AA Chassis = 6
HEPI AI Chassis = 6
Filter Boards = 76
Binary In Chassis = 4
Binary Out Chassis = 3

ISI 6

HEPI 1

HEPI 6

HAMs 2&3 L1-SEI-C2



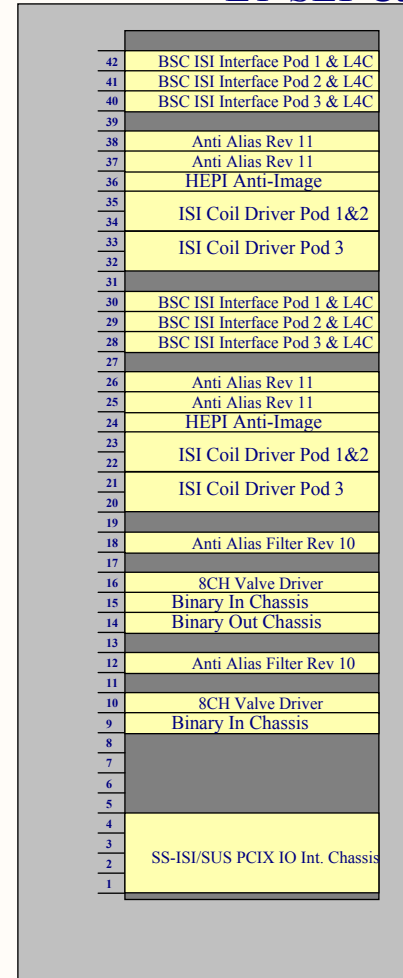
ISI 2

ISI 3

HEPI 2

HEPI 3

HAMs 4&5 L1-SEI-C3



ISI 4

ISI 5

HEPI 4

HEPI 5

Title aLIGO HAM ISI System Wiring			
Size: B	DCC Number: D1000298		Ligo Project California Institute of Technology Massachusetts Institute of Technology
Drawn by: Ben Abbott	Date: 9/11/2012	Revision: v5	Cannot open file C:\Ben\miscellan y\ligologo_1.jpg
File: C:\restored\Ben\Seismic\ISI\Single Stage ISI\HAM4_5_system_sch\d1000298.dwg			