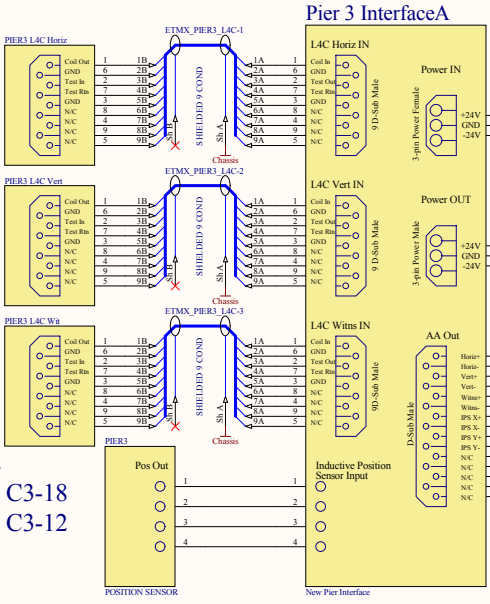
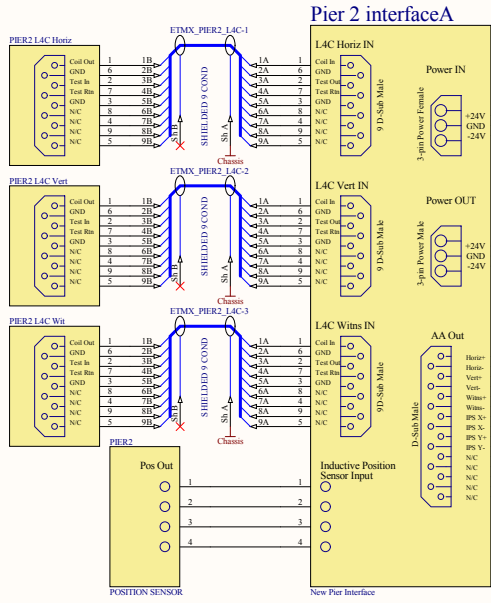


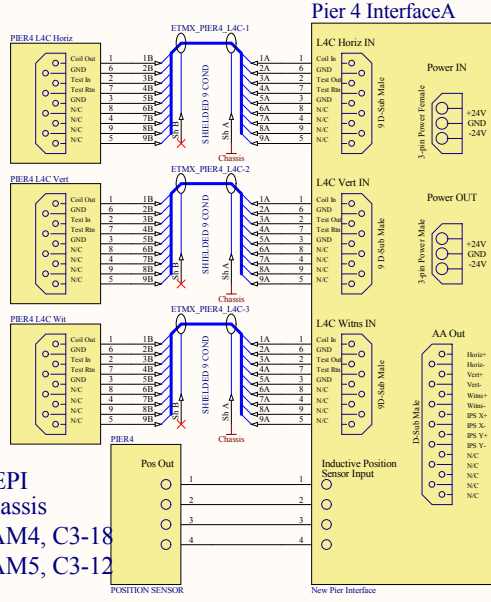
To HEPI AA Chassis for HAM4, C3-18 for HAM5, C3-12



To HEPI AA Chassis for HAM4, C3-18 for HAM5, C3-12



To HEPI AA Chassis for HAM4, C3-18 for HAM5, C3-12



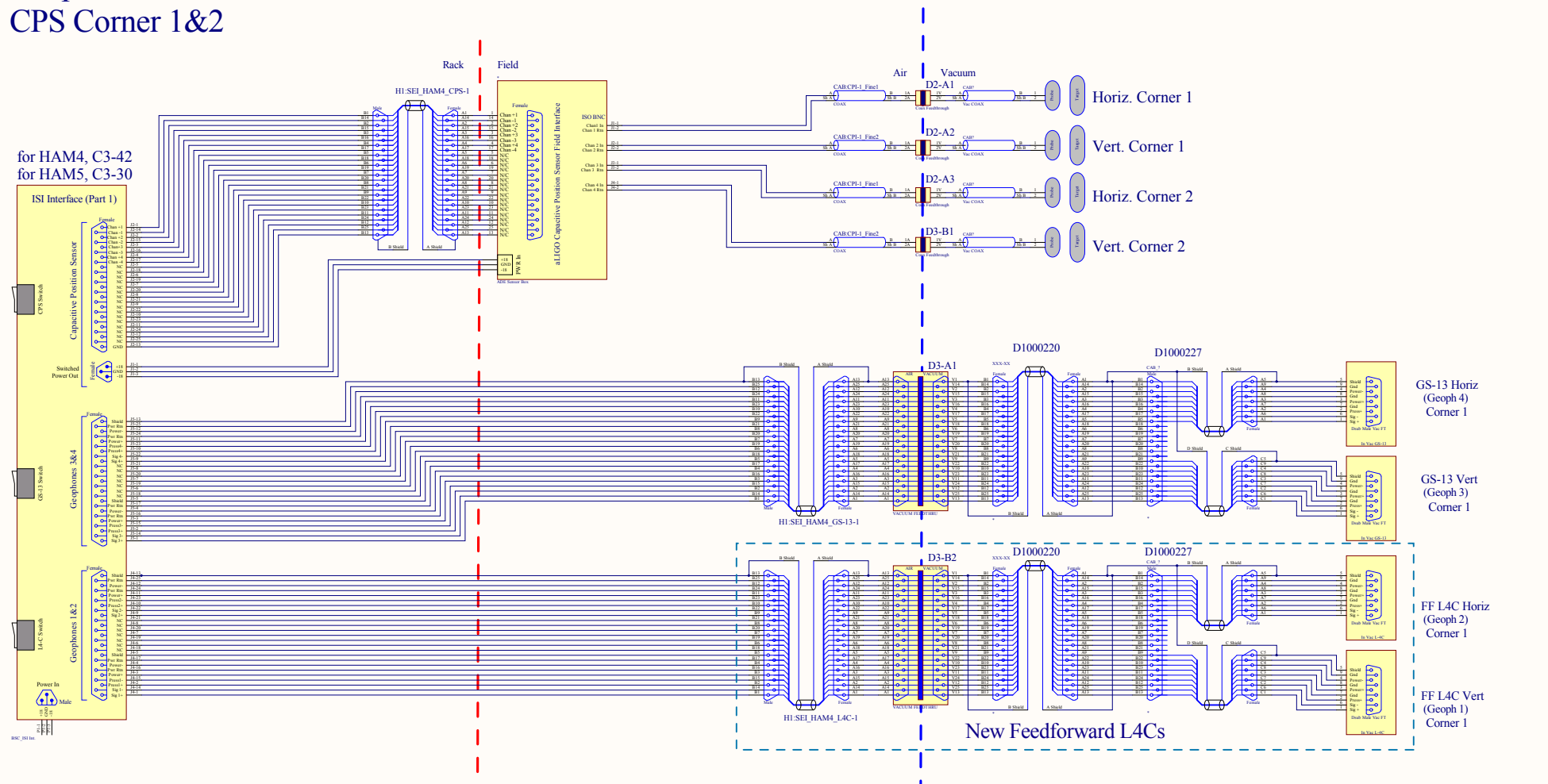
To HEPI AA Chassis for HAM4, C3-18 for HAM5, C3-12

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| Title: alIGO HAM ISI HEPI System Schematics | | | |
| Size: C | DCC Number: D1000298 | Ligo Project California Institute of Technology Massachusetts Institute of Technology | |
| Drawn by: Ben Abbott | Date: 8/30/2011 | Revision: v4 | Cannot open file C:\Ben\misc\lan\lyligo_1.jpg |
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Geophones Corner 1

CPS Corner 1&2

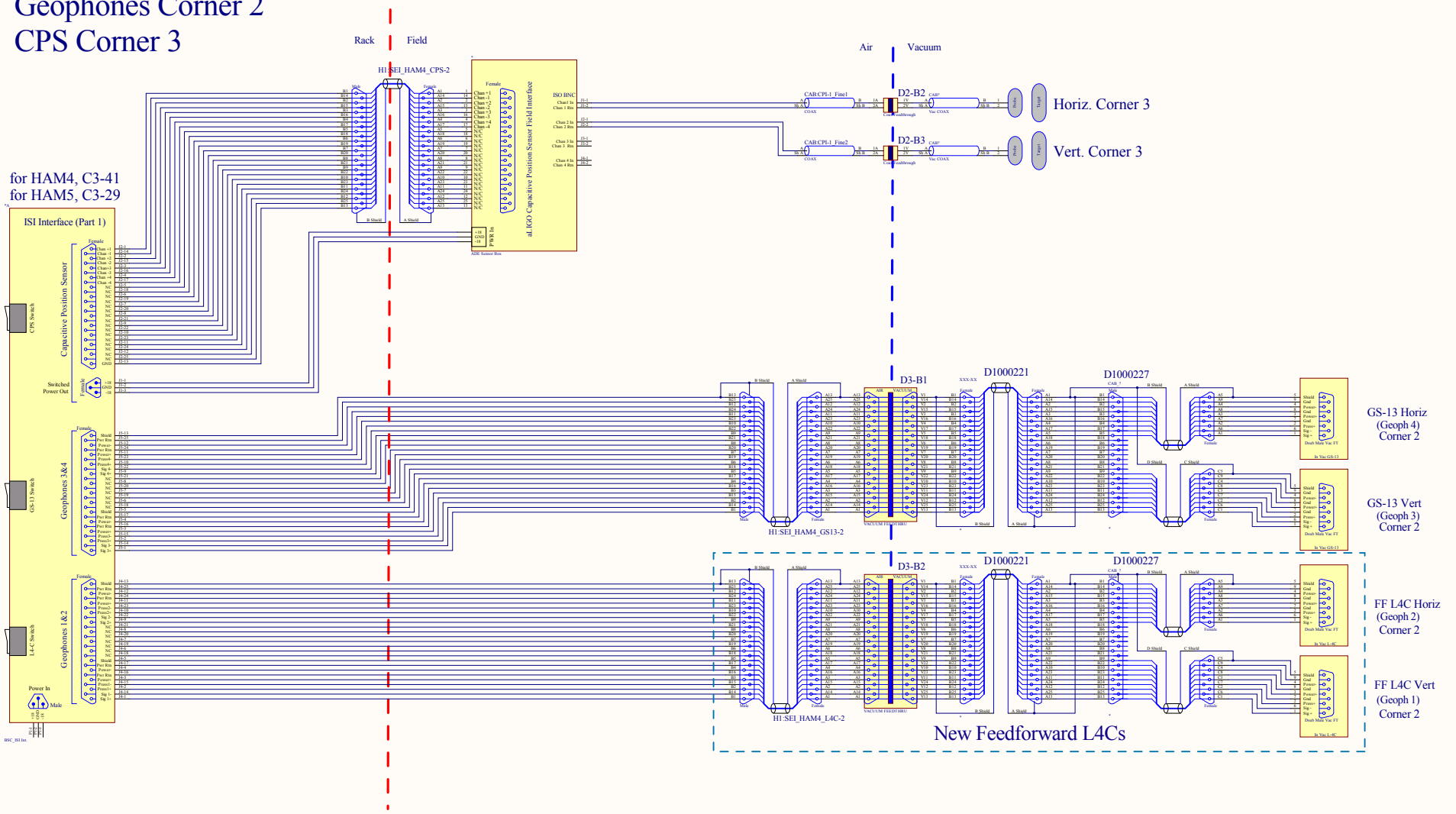
for HAM4, C3-42
for HAM5, C3-30



New Feedforward L4Cs

Geophones Corner 2 CPS Corner 3

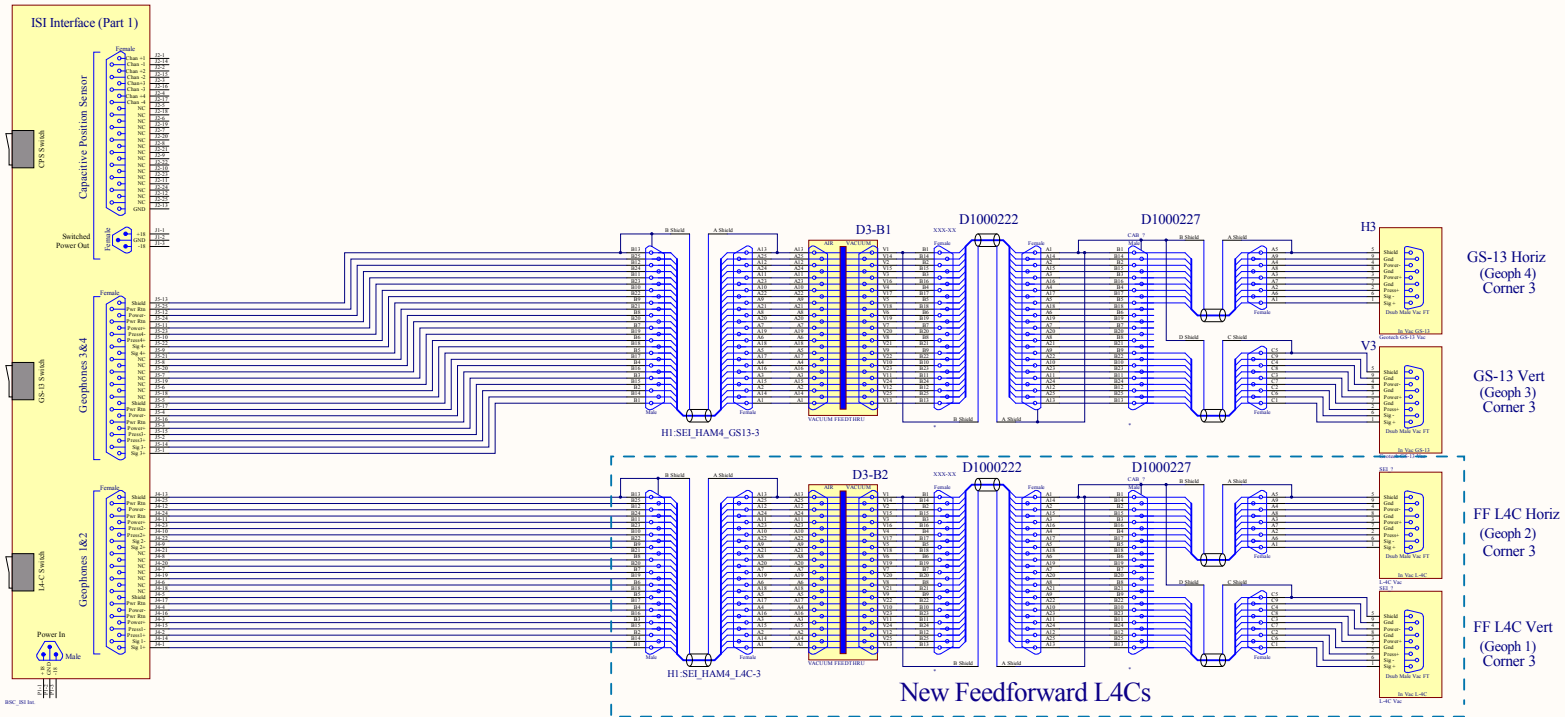
for HAM4, C3-41
for HAM5, C3-29



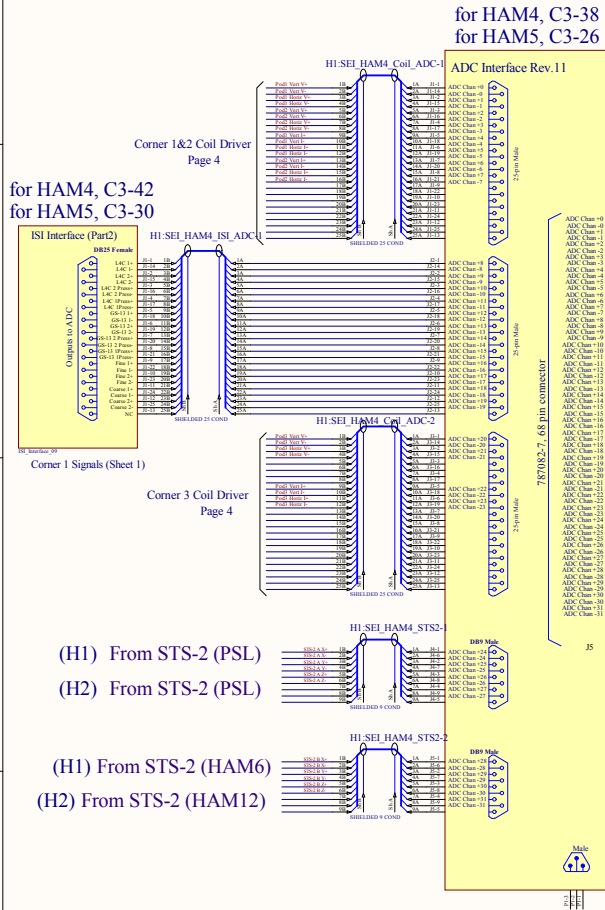
New Feedforward L4Cs

Geophones Corner 3

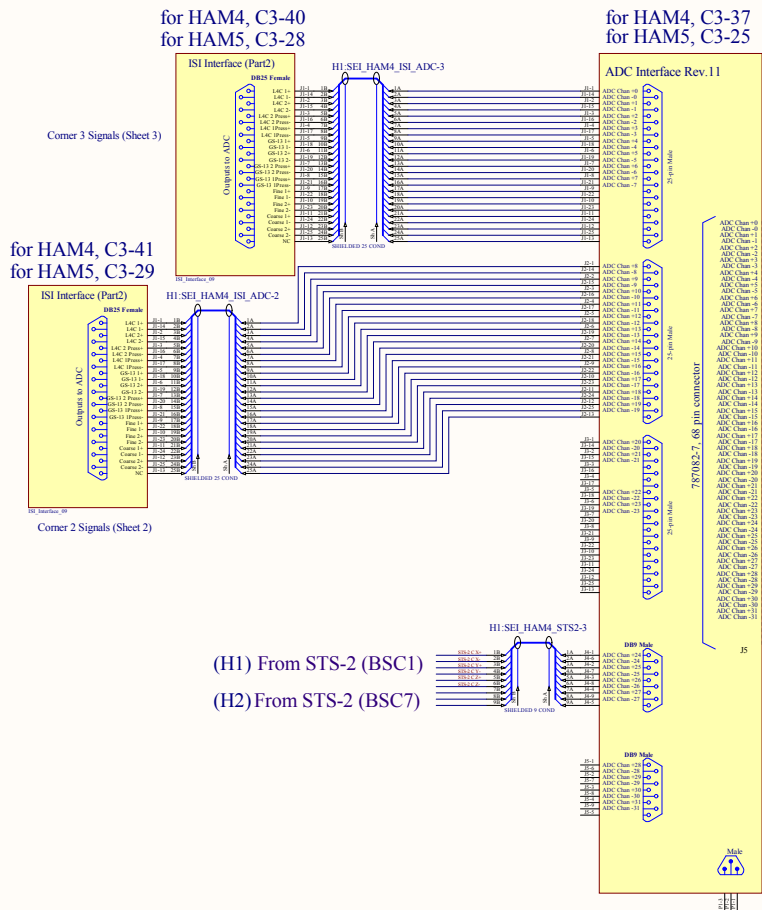
for HAM4, C3-40
for HAM5, C3-28



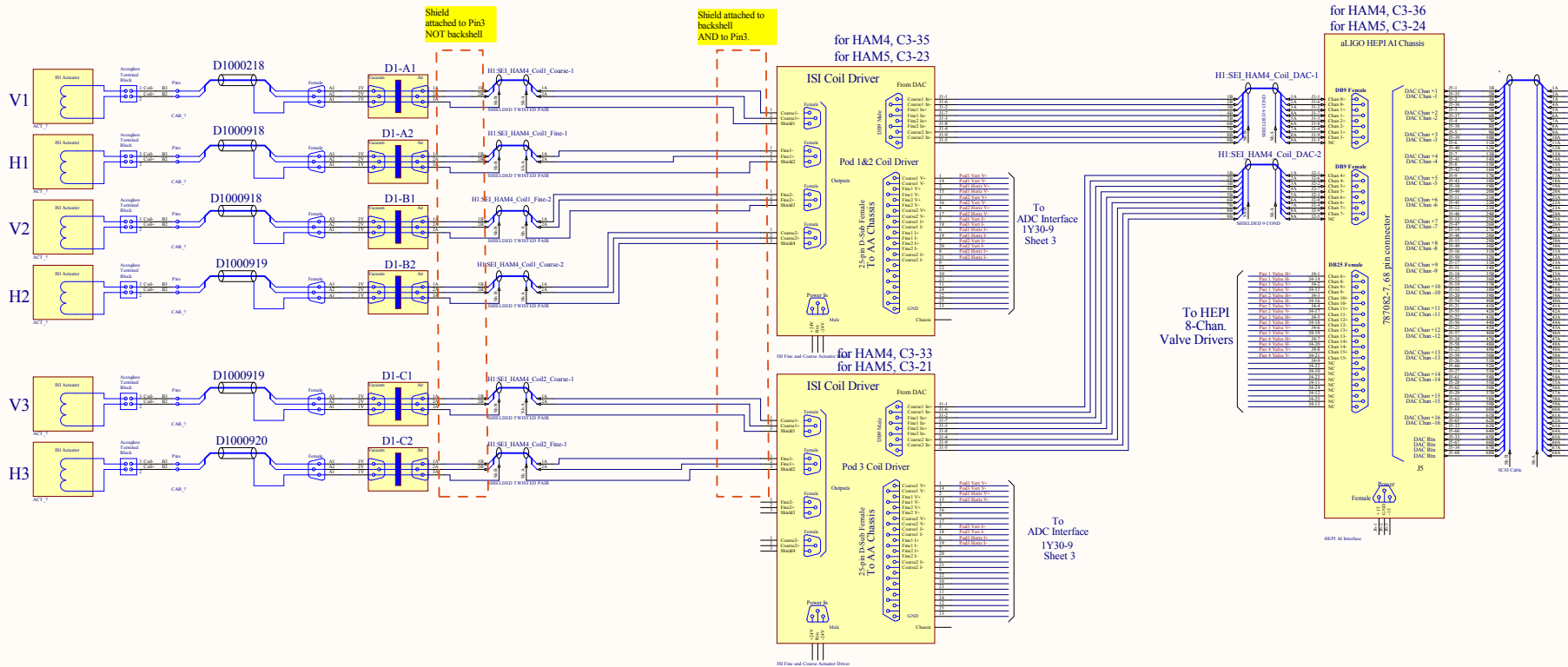
HAMs 4&5 ISI ADCs



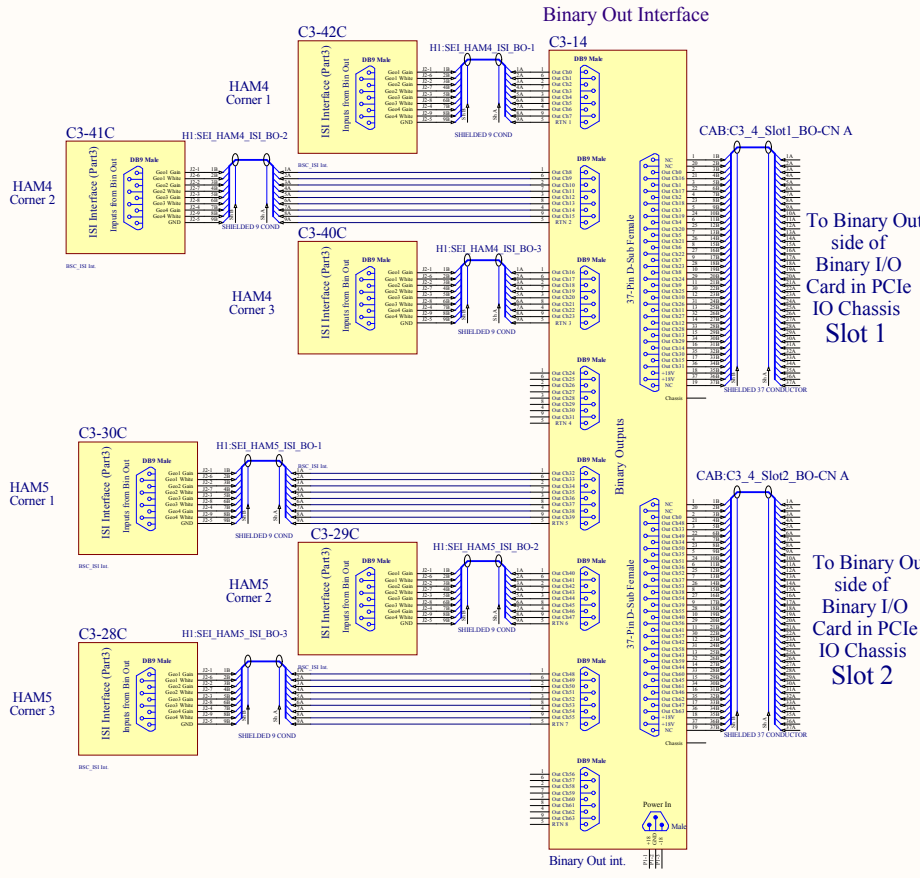
General Standards ADC
for HAM4, IO Chassis Slot 6
for HAM5, IO Chassis Slot 8



General Standards ADC
for HAM4, IO Chassis Slot 7
for HAM5, IO Chassis Slot 9

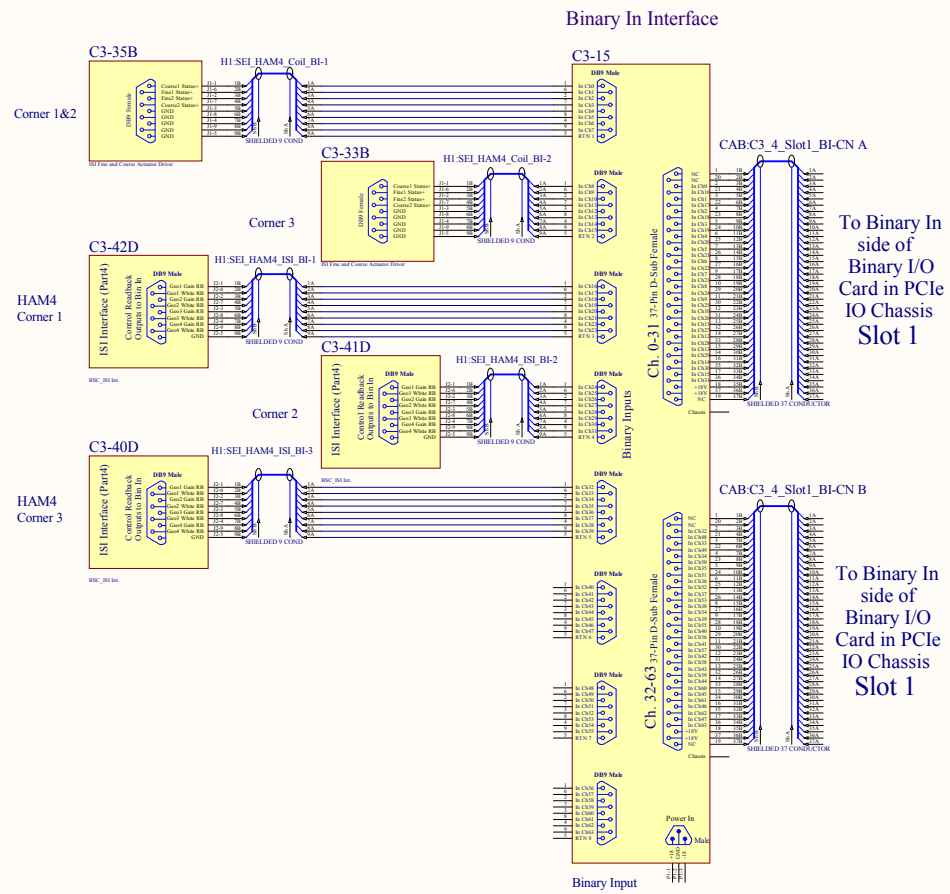


General Standards DAC
 for HAM4, IO Chassis Slot 10
 for HAM5, IO Chassis Slot 11



To Binary Out side of Binary I/O Card in PCIe IO Chassis Slot 1

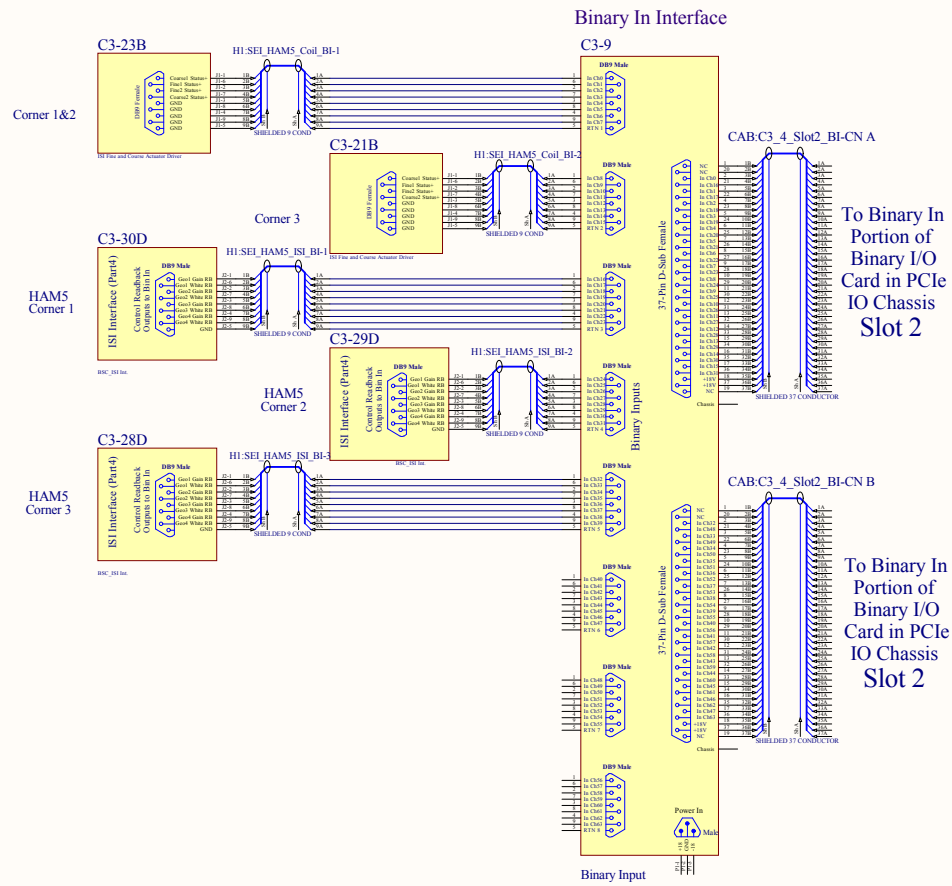
To Binary Out side of Binary I/O Card in PCIe IO Chassis Slot 2



To Binary In side of Binary I/O Card in PCIe IO Chassis Slot 1

To Binary In side of Binary I/O Card in PCIe IO Chassis Slot 1

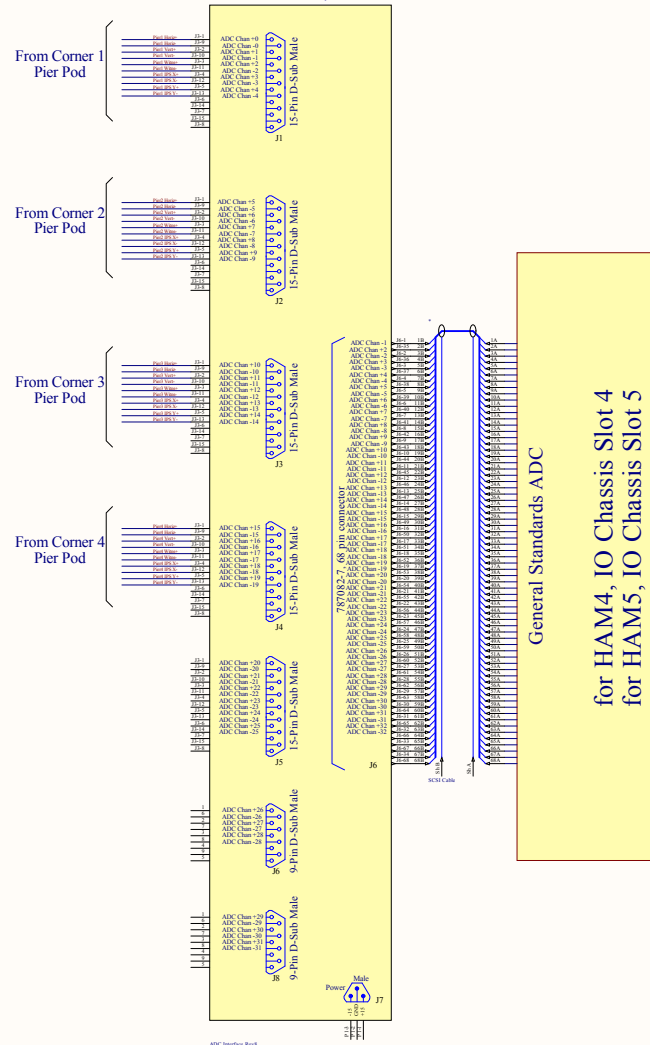
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|------------------------------|-----------------------|---------------|---------------------------|
| al-ISO HAM ISI System Wiring | | | |
| Rev: | Doc Number: 01000208 | App. Project: | Customer Name: Technology |
| Rev. D | | Doc. Name: | Customer Name: Technology |
| Doc. Title: | Doc. Date: 01/09/2013 | Revision: | Doc. No.: |
| Doc. No.: | | Doc. No.: | |



To Binary In Portion of Binary I/O Card in PCIe IO Chassis Slot 2

To Binary In Portion of Binary I/O Card in PCIe IO Chassis Slot 2

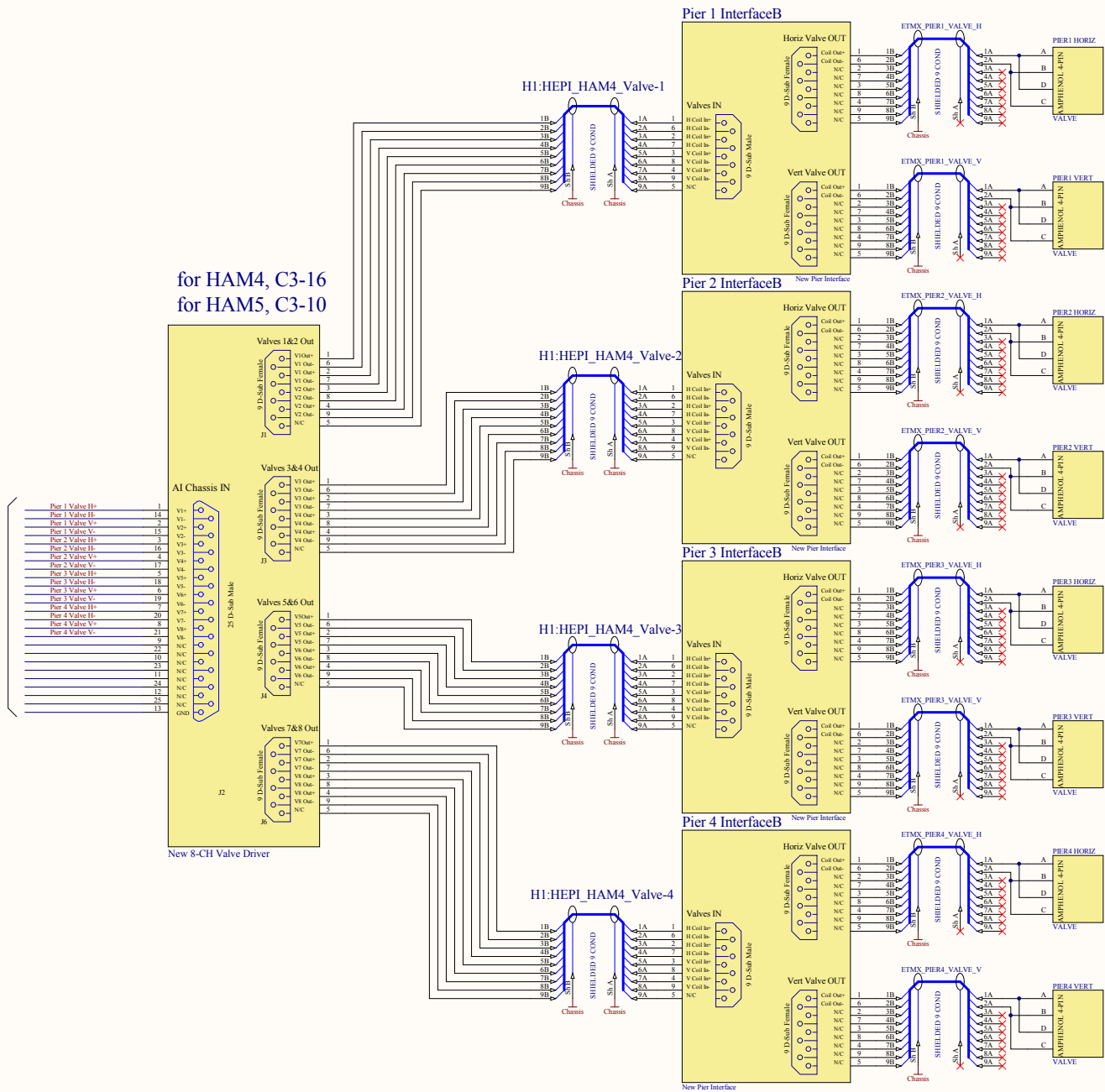
for HAM4, C3-18
for HAM5, C3-12



General Standards ADC
for HAM4, IO Chassis Slot 4
for HAM5, IO Chassis Slot 5

From AI Chassis
for HAM4, C3-36
for HAM5, C3-24
Page 5

for HAM4, C3-16
for HAM5, C3-10



New 8-CH Valve Driver

| | | | |
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| Title ALIGO HAM ISI System Schematics | | | |
| Size: C | DCC Number: D100298 | Ligo Project California Institute of Technology Massachusetts Institute of Technology | Cannot open file C:\Ben\misc\lan yloglog_1.jpg |
| Drawn by: Ben Abbott | Date: 8/30/2011 | Revision: v4 | |
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