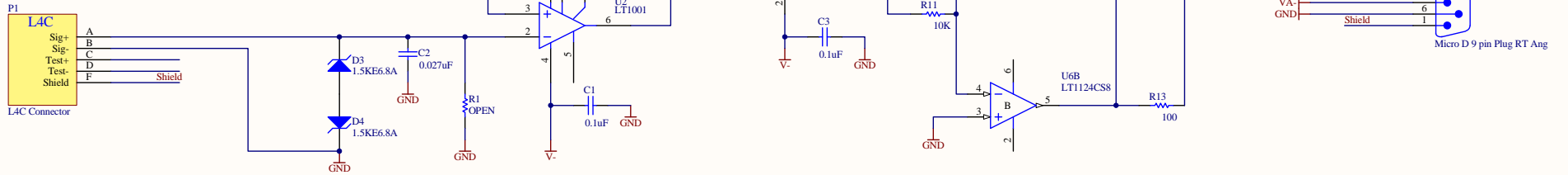
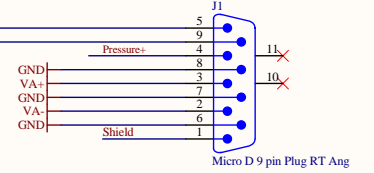


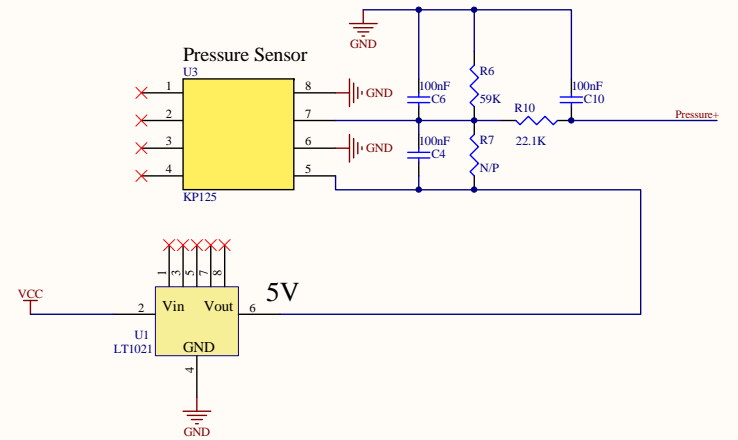
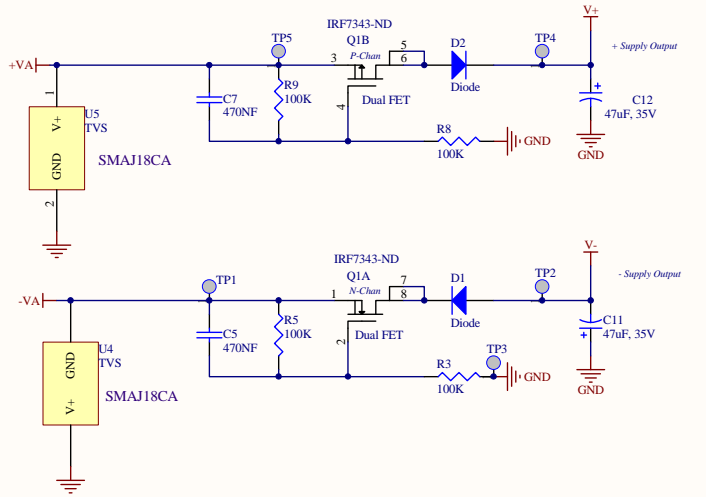
Pin A--- Coil +  
 Pin B--- Coil -  
 Pin C--- Cal Coil +  
 Pin D--- Cal Coil -  
 Pin F--- Case GND/Shield



Note: DB9 not stuffed. Pads used to land wires from internal cable



Inrush Current Limiter, Transient spike, and reverse Voltage protection.



Title <b>ISI LAC Interface Board</b>		LIGO Project California Institute of Technology Massachusetts Institute of Technology		LIGO	
Size: B	DCC Number: D1000059	SCH / PCB Revision: v1	Engineer: Sam Abbott	Date: 1/20/2010	
File: C:\Documents and Settings\sabbott\My Documents\My Designs\NewLACPreamp (1-6-2010 2:50-11 PM)\NewLACPreamp.sch					