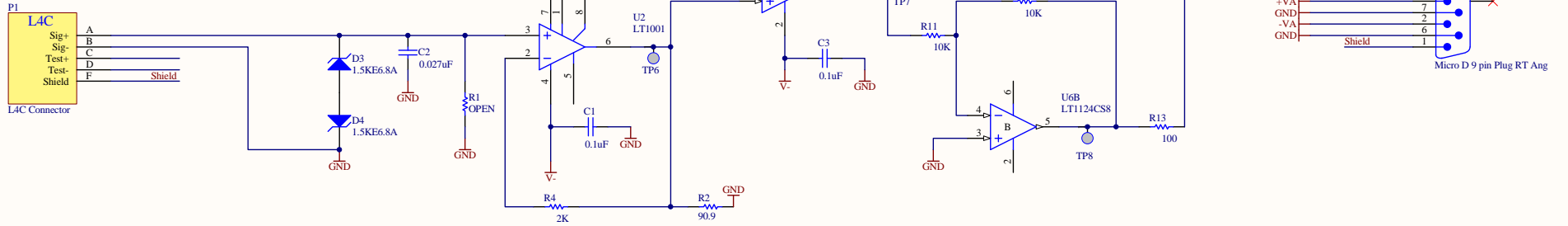
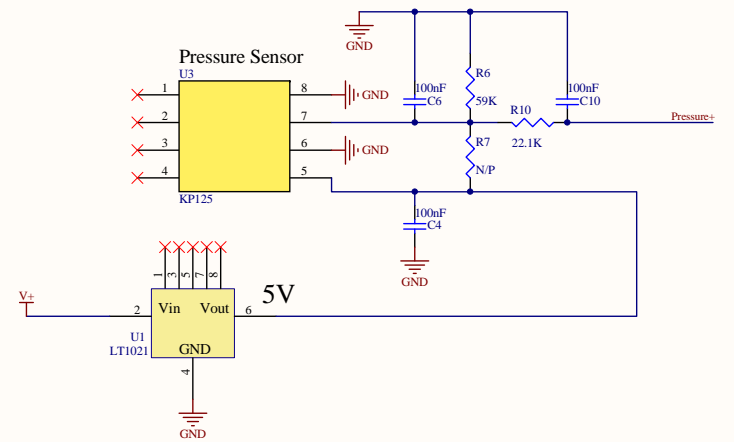
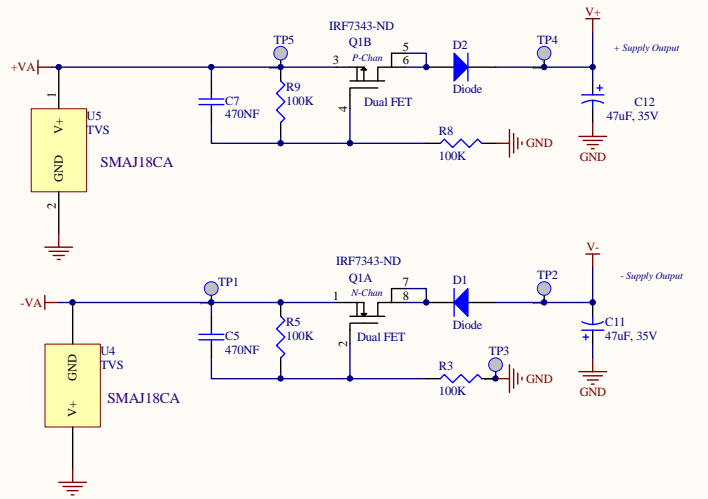


Pin A--- Coil +  
 Pin B--- Coil -  
 Pin C--- Cal Coil +  
 Pin D--- Cal Coil -  
 Pin F-- Case GND/Shield



Inrush Current Limiter, Transient spike, and reverse Voltage protection.



|   |                      |   |                      |                 |  |
|---|----------------------|---|----------------------|-----------------|--|
| Title<br><b>ISI LAC Interface Board</b>   |                      | LIGO Project<br>California Institute of Technology<br>Massachusetts Institute of Technology |                      | LIGO            |  |
| Size: B   | DCC Number: D1000059 | SCH / PCB Revision: v1  | Engineer: Sam Abbott | Date: 3/29/2010 |  |
| File: C:\Documents and Settings\sabbott\My Documents\My Designs\NewLACPreamp (1-6-2010 2:50:11 PM)\LACPreamp3-182010 SchDoc |                      |   |                      |                 |  |